

Towards Certified Compositional Compilation for Concurrent Programs

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Abstract

Certified compositional compilation is important for establishing end-to-end guarantees for certified systems consisting of separately compiled modules. In this paper, we propose a framework consisting of the key semantics components and verification steps that bridge the gap between the compilers for sequential programs and for (race-free) concurrent ones, so that the existing efforts on certified sequential compilation can be reused for concurrent programs. Contributions of the framework include an abstract formulation of race-freedom in an interaction semantics, and a footprint-preserving compositional simulation as the compilation correctness criterion.

1. Introduction

Address the problem, its importance and challenges. Certified compositional compilation has been considered an important (though challenging) problem [\[\[by whom?\]\]](#). A real-world program usually consists of multiple modules which are compiled independently. Correct compilation is expected to have compositionality, ensuring that separately compiled modules can work together and preserve the semantics of the source program as a whole. That is, we need not only that each single target module preserves the semantics of its source, but also that the *interactions* between the source modules are correctly reflected at the target. With the presence of concurrency, the problem becomes much harder since we have to consider non-deterministic interleavings between threads as well.

Advance over previous work. Although there are already many works on certified separate compilation, most of them deal with compilation for sequential programs. The first realistic certified compiler, CompCert [1], establishes the semantic preservation property for *closed sequential* programs. It can guarantee the correctness of separate compilation when the sequential modules do not interact with each other. To support general separate compilation, Stewart et al. [4] developed Compositional CompCert, which allows the modules to call each other's external functions. Their approach relies on *non-preemptive* semantics, where each module is interacted with others only at certain program points specified in the module code (i.e., at external calls only). It is unclear if their approach can also be applied to interleaving concurrency, where a module may be preempted by others at arbitrary and non-deterministically-chosen program points.

On the other hand, existing works on certified compilation for concurrent programs are usually not compositional (e.g., [2, 3], which we will discuss in Sec. 9), thus cannot be applied to separate compilation. As we will see in detail in Sec. 2, it is non-trivial to build certified compositional compilation for preemptive concurrent programs.

In this paper, we propose a framework consisting of the key semantics components and verification steps that bridge the gap between compilation for sequential programs and for (race-free) concurrent ones. The key ingredient here is data-race-free (DRF) assumption. We first formally prove the folklore theorem about the equivalence between the preemptive and non-preemptive semantics for race-free programs, so that it is possible to derive the semantics preservation between preemptive programs from the semantics preservation between non-preemptive ones when both source and target programs are race-free. Then we prove DRF preservation in a compositional style, using a notion of footprint-preservation, thus DRF of target program could be derived from DRF of the source. Finally, together with the semantics preservation proof between non-preemptive programs, we are able to derive the semantics preservation result between preemptive programs, assuming the source is race free.

Contribution. Our work is built upon earlier work on CompCert [1] and Compositional CompCert [4], but makes the following new contributions:

- We design a footprint-preserving simulation as the correctness of compilation for individual modules. As an extension of the structured simulation in Compositional CompCert, our simulation considers interactions at both external function calls and synchronization points, thus is compositional with respect to both module linking and non-preemptive parallelism. It also requires that the footprints of the steps made by the source and target modules be related, which is the key to derive DRF preservation between the source and target whole programs in a compositional style.
- To bridge the gap between our simulations in the non-preemptive semantics and the DRF preservation we need for preemptive programs, we formulate a novel notion of DRF in the non-preemptive semantics, denoted as NPDRF. We show that a program satisfies our NPDRF in the non-preemptive semantics if and only if it satisfies the standard DRF notion in the interleaving semantics. Then DRF preservation can be derived from NPDRF preservation, which is ensured by footprint preservation in our thread-local and module-local simulations.
- Putting all these together, our framework (see Fig. 3) is the first to build certified compositional compilation for concurrent programs from sequential compilation. It highlights the importance of DRF preservation for correct compilation. It also gives the essential semantic requirements for the proof of equivalence between preemptive and non-preemptive semantics.
- As an instantiation of our language independent compilation framework, we extend the CompCert Clight with a set of C11-like SC-atomic primitives as source language, and x86 assembly with lock prefix as target language. We successfully

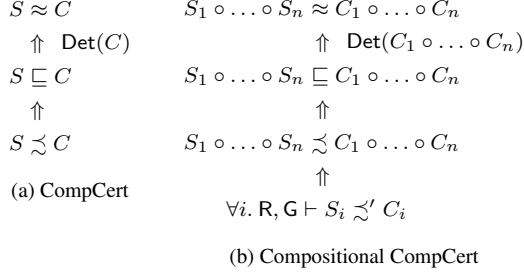


Figure 1. Proof structures of certified compilation.

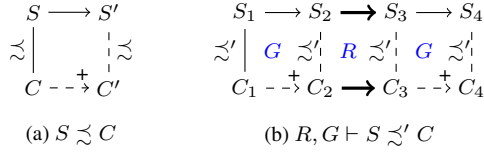


Figure 2. Simulation diagrams.

proved the CompCert compiler and the atomic primitives with their implementations satisfying our correctness criteria. Thus Clight modules compiled by CompCert linked with the atomic primitives are proved to preserve the behavior of the (DRF) source program as a whole.

[[[Is it possible to bypass Compositional CompCert and reuse CompCert proofs directly without major changes? Are we able to claim it is a "lightweighted" approach compared with Compositional CompCert/ CompCertTSO?]]]

- In this instantiation, the only assumptions we made on source programs is DRF. We provide various ways for proving DRF property including a static race checker and a simplified separation logic that are sound with respect to data-race freedom. With an automatic race checker, the entire proof of semantics preservation could be generated with simply one click.

In the rest of this paper, we first analyze the challenge and give an overview of our approach in Sec. 2. We give the basic technical setting in Sec. 3, including the footprint-instrumented preemptive semantics and the refinement definition. Sec. 4 presents the non-preemptive semantics, which is the basis for both our new simulation (Sec. 14) and the NPDRF definition (Sec. 7). We show the final theorem in Sec. 8, and discuss related work in Sec. 9.

2. Informal Development

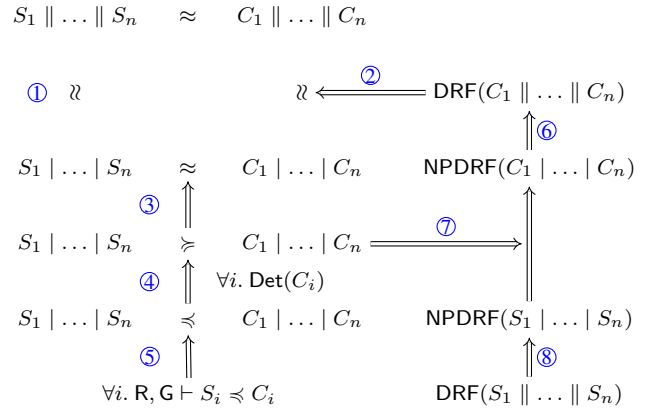


Figure 3. Our framework

3. Basic Technical Settings

3.1 The Abstract Language

<i>(Prog)</i>	$P, \mathbb{P} ::= \mathbf{let} \Pi \mathbf{in} f_1 \parallel \dots \parallel f_n$
<i>(Entry)</i>	$f \in \mathit{String} \quad (TID) \mathfrak{t} \in \mathbb{N}$
<i>(MdSet)</i>	$\Pi, \Gamma ::= \{(t_1, \pi_1), \dots, (t_m, \pi_m)\}$
<i>(Lang)</i>	$tl, sl ::= (Module, Core, \mathit{InitCore}, \mapsto, \mathit{AftExtn})$
<i>(Module)</i>	$\pi, \gamma ::= \dots$
<i>(Core)</i>	$\kappa, \mathbb{k} ::= \dots$
$\mathit{InitCore}$	$\in Module \rightarrow Entry \rightarrow list(Val) \rightarrow Core$
\mapsto	$\in (Module \times FList) \times (Core \times State) \rightarrow \{\mathcal{P}((Msg \times FtPrt) \times (Core \times State)), \mathbf{abort}\}$
$\mathit{AftExtn}$	$\in Core \times Value \rightarrow Core$
<i>(BlockID)</i>	$b \in \mathbb{N}$
<i>(State)</i>	$\sigma, \Sigma \in BlockID \rightarrow_{\mathit{fin}} \mathbb{N} \rightarrow Value$
\mathcal{U}	$l ::= (b, i) \quad \text{where } i \in \mathbb{N}$
<i>(Value)</i>	$v ::= l \mid \dots$
<i>(BSet)</i>	$S, \mathbb{S} \in \mathcal{P}(BlockID)$
<i>(FList)</i>	$F, \mathbb{F} \in \mathcal{P}^\omega(BlockID)$
<i>(FSpace)</i>	$FS, \mathbb{F}\mathbb{S} ::= F :: FS \quad (\text{co-inductive})$
<i>(FtPrt)</i>	$\delta, \Delta ::= (rs, ws) \quad \text{where } rs, ws \in \mathcal{P}(\mathcal{U})$
<i>(Msg)</i>	$\iota ::= \tau \mid e \mid \mathbf{call}(f, \vec{v}) \mid \mathbf{ret}(v) \mid \mathbf{entA} \mid \mathbf{extA}$
<i>(Event)</i>	$e ::= \dots$
<i>(Config)</i>	$\phi, \Phi ::= (\kappa, \sigma) \mid \mathbf{abort}$

$\mathit{locs}(\sigma) \stackrel{\text{def}}{=} \{(b, i) \mid b \in \mathit{dom}(\sigma) \wedge i \in \mathit{dom}(\sigma(b))\}$
$\llbracket S \rrbracket \stackrel{\text{def}}{=} S \times \mathbb{N}$
$\mathit{forward}(\sigma, \sigma') \text{ iff } (\mathit{dom}(\sigma) \subseteq \mathit{dom}(\sigma')) \wedge (\mathit{locs}(\sigma') \cap \llbracket \mathit{dom}(\sigma) \rrbracket \subseteq \mathit{locs}(\sigma))$
$\sigma \stackrel{rs}{=} \sigma' \text{ iff } \forall (b, i) \in rs. ((b, i) \notin \mathit{locs}(\sigma) \cup \mathit{locs}(\sigma')) \vee ((b, i) \in \mathit{locs}(\sigma) \cap \mathit{locs}(\sigma')) \wedge (\sigma(b)(i) = \sigma'(b)(i))$
$\delta \subseteq \delta' \text{ iff } (\delta.rs \subseteq \delta'.rs) \wedge (\delta.ws \subseteq \delta'.ws)$
$\delta \cup \delta' \stackrel{\text{def}}{=} (\delta.rs \cup \delta'.rs, \delta.ws \cup \delta'.ws)$

Figure 4. The Abstract Concurrent Language

Fig. 4 show the syntax of an abstract language for preemptive concurrent programming, which can be instantiated to various concrete and practical languages.

A program P consists of several threads running in parallel. Each thread executes a module declared in Π , starting from an entry f . Each module π may contain several entries. Different modules may be written in different languages. We define a module language tl as a tuple $(Module, Core, \mathit{InitCore}, \mapsto)$. $Module$ describes the syntax of the modules in this language. Following Compositional CompCert [4], $Core$ is the set of internal “core” states, which can be instantiated to control continuations, instruction streams, register files, etc.. Given a module π and an entry f , the function $\mathit{InitCore}$ returns the initial “core” state κ (it is undefined if the entry is not contained in the module). We assume that the entries in different modules are different.

A memory state σ is defined as a partial function of the type $\mathcal{U} \rightarrow Value$, where \mathcal{U} represents the type of locations. To simplify the presentation, we specialize \mathcal{U} to be the set of memory locations in the CompCert memory model [?], where memory is organized as a collection of blocks. A memory location l is a pair (b, i) of

a block b and an offset i within this block. The purpose of this memory model is to allow pointer arithmetic only within the same block. Note that our framework actually does not rely on this special instantiation of \mathcal{U} . It can be applied to other memory models.

Besides, we assume that every thread is created with a free list F (where $F \subseteq \mathcal{U}$), used to allocate memory for the thread. The free lists of different threads are disjoint, ensuring that the allocation operations made by different threads never race. To simplify the presentation, we assume that free lists are infinite. In the case of CompCert memory model, F should contain an infinite number of blocks where each block contains all the possible offsets. Here $\mathcal{P}(\cdot)$ denotes the power set. **[[[No need to assume the special form of free lists? Besides, where do we *use* the assumption that F is infinite?]]]**

Footprint-based semantics. The operational semantics of a module is defined using the labeled transitions $(\pi, F) \vdash (\kappa, \sigma) \xrightarrow[\delta]{\iota} (\kappa', \sigma')$ (or $(\pi, F) \vdash (\kappa, \sigma) \xrightarrow[\delta]{\iota} \mathbf{abort}$ if the step goes wrong). We write ϕ for the configuration (κ, σ) or \mathbf{abort} . Each step is labeled with a message ι and a footprint δ . The messages contain information about the module-local steps. To simplify the presentation, we only consider externally observable events e (such as outputs), termination \mathbf{halt} , and the starts and ends of atomic blocks \mathbf{entA} and \mathbf{extA} . Any other step is silent, labeled with τ (which is often omitted in the following presentation). The messages define the protocols of communications with the global (whole-program) semantics (which will be described soon), so it is natural to require all the module languages to use the same message formats. They allow us to abstract away the syntax and semantics details of the module languages, and focus on the interactions with other modules and the external observer (the latter can observe e only). We can also extend ι with messages of external function calls, and extend the global semantics to handle them. But supporting external functions is orthogonal to our work on certified compilation of concurrent programs, so we do not present it here.

The footprint δ is defined as a pair (rs, ws) , which records the memory locations that are read and written in this step. Recording the footprint allows us to discuss races between threads in the following sections. We write \mathbf{emp} for the special footprint where both the read and write sets are empty.

We require the language semantics to be well-defined (see wd in Def. 1 below). First, a step may enlarge the memory domain but cannot reduce it, and the additional memory should be allocated from F . This requirement follows the CompCert memory model where memory disposals do not really remove the locations from the memory (they just become invalid). Second, the footprints should be included in the domain of the memory, and the newly allocated memory should be included in the write sets. Besides, the memory out of the write sets should keep unchanged, as described by $\sigma \xrightarrow[\delta]{\mathit{dom}(\sigma) - \delta.ws} \sigma'$ (which is defined at the bottom of Fig. 4). Finally, the memory updates at the write sets of a step only depends on the read sets.

Definition 1 (Well-Defined Languages). $\mathit{wd}(tl)$ iff for any $\pi, F, \kappa, \sigma, \iota, \kappa', \sigma'$ and δ , if $(\pi, F) \vdash (\kappa, \sigma) \xrightarrow[\delta]{\iota} (\kappa', \sigma')$, then all of the following hold (some auxiliary definitions are in Fig. 5):

- (1) $\mathit{forward}(\sigma, \sigma')$;
- (2) $\mathit{LEffect}(\sigma, \sigma', \delta, F)$
- (3) for any σ_1 , $\mathit{LEqPre}(\sigma, \sigma_1, \delta, F)$, then there exists σ'_1 such that $(\pi, F) \vdash (\kappa, \sigma_1) \xrightarrow[\delta]{\iota} (\kappa', \sigma'_1)$ and $\mathit{LEqPost}(\sigma', \sigma'_1, \delta, F)$.
- (4) for any δ_0 and σ_1 , if

$$\delta_0 = \bigcup \{ \delta'' \mid \exists \kappa'', \sigma''. (\pi, F) \vdash (\kappa, \sigma) \xrightarrow[\delta'']{\tau} (\kappa'', \sigma'') \},$$

$$\begin{aligned}
\text{LEqPre}(\sigma_1, \sigma_2, \delta, F) &\stackrel{\text{def}}{=} \sigma_1 \xrightarrow{\delta, rs} \sigma_2 \wedge \\
&\text{locs}(\sigma_1) \cap \delta, ws = \text{locs}(\sigma_2) \cap \delta, ws \wedge \\
&\text{dom}(\sigma_1) \cap F = \text{dom}(\sigma_2) \cap F \\
\text{LEqPost}(\sigma'_1, \sigma'_2, \delta, F) &\stackrel{\text{def}}{=} \sigma'_1 \xrightarrow{\delta, ws} \sigma'_2 \wedge \\
&\text{dom}(\sigma'_1) \cap F = \text{dom}(\sigma'_2) \cap F \\
\text{LEffect}(\sigma_1, \sigma_2, \delta, F) &\stackrel{\text{def}}{=} \sigma_1 \xrightarrow{\text{locs}(\sigma_1) - \delta, ws} \sigma_2 \wedge \\
&\text{locs}(\sigma_1) - \text{locs}(\sigma_2) \subseteq \delta, ws \wedge \\
&\text{locs}(\sigma_2) - \text{locs}(\sigma_1) \subseteq \delta, ws \wedge \\
&\text{dom}(\sigma_2) - \text{dom}(\sigma_1) \subseteq F
\end{aligned}$$

Figure 5. Auxiliary definitions for well-defined languages

$\text{LEqPre}(\sigma, \sigma_1, \delta_0, F)$ then

$$\begin{aligned}
\forall \kappa''_1, \sigma''_1, \iota''_1, \delta''_1. (\pi, F) \vdash (\kappa, \sigma_1) \xrightarrow{\iota''_1}_{\delta''_1} (\kappa''_1, \sigma''_1) &\implies \\
\exists \sigma''. (\pi, F) \vdash (\kappa, \sigma) \xrightarrow{\iota''_1}_{\delta''_1} (\kappa''_1, \sigma'') &
\end{aligned}$$

- (5) if $\iota = \text{call}(\mathbf{f}, \vec{v})$, then $\forall v'. \exists \kappa''. \text{AftExtn}(\kappa', v') = \kappa''$;
if $\iota = \text{entA} \vee \iota = \text{extA}$, then $\forall v'. \text{AftExtn}(\kappa', v') = \kappa'$.

Definition 2 (Deterministic Languages). $\text{det}(tl)$ iff

$$\begin{aligned}
\forall \pi, F, \phi, \phi_1, \phi_2, \iota_1, \iota_2, \delta_1, \delta_2. \\
(\pi, F) \vdash \phi \xrightarrow{\iota_1}_{\delta_1} \phi_1 \wedge (\pi, F) \vdash \phi \xrightarrow{\iota_2}_{\delta_2} \phi_2 &\implies \\
\phi_1 = \phi_2 \wedge \iota_1 = \iota_2 \wedge \delta_1 = \delta_2 &
\end{aligned}$$

Below we write $(\pi, F) \vdash \phi \xrightarrow{\tau}_{\delta} \phi'$ for multiple silent-step transitions, where δ is the accumulation of the footprints generated. $(\pi, F) \vdash \phi \xrightarrow{\tau}_{\delta}^* \phi'$ is for zero or multiple silent-step transitions, where δ is emp for the case of zero step. Similarly, for global steps, we write $W \xrightarrow{\tau}_{\delta}^+ W'$ for multiple silent-step transitions. Besides, we also write $W \Rightarrow^+ W'$ for multiple steps that either are silent or produce sw events. It must contain at least one silent step. The meanings of $W \Rightarrow^+ \mathbf{abort}$ and $W \Rightarrow^+ \mathbf{done}$ are similar. $W \xrightarrow{e}_{\delta}^+ W'$ represents multiple steps with exactly one e event produced (where other steps either are silent or produce sw events).

Other assumptions. In addition to the above requirements on module languages, we also assume that any non-silent transition (i.e., labeled with e , entA , extA or halt) does not access or update the memory state, so its footprint is emp . **[[[The assumption that e-step's footprint is empty looks strong...]]]** Besides, the steps producing entA and extA should be paired. Also, we do not allow nested atomic blocks or outputs inside atomic blocks (i.e., the steps producing e or entA never happen between an entA step and an extA step).

Note that we do not enforce these assumptions, but our global semantics (see below) would *abort* if no transitions of the module can satisfy these assumptions. **[[[Can it just “get stuck”?]]]**

Global semantics. Fig. 7 defines a set of global semantics rules to manipulate the preemption among threads. As shown in Fig. 6(a), the global world W consists of the thread pool T , the ID τ of the thread currently being executed, a bit d indicating whether the current thread is in an atomic block or not, and the memory state σ . The thread pool T contains the runtime module K of every thread, which records the module language tl , the module code π , the free list F created for the thread, and the current “core” state κ . Since we assume that there is no external function calls, each thread would

$$\begin{aligned}
(\text{World}) \quad W, \mathbb{W} &::= ((\Pi, T), (\tau, d, \sigma, FS)) \\
(\text{ThrdPool}) \quad T, \mathbb{T} &::= \{\tau_1 \rightsquigarrow K_1, \dots, \tau_n \rightsquigarrow K_n\} \\
(\text{RtMdStk}) \quad K, \mathbb{K} &::= \epsilon \mid ((tl, \pi, F), \kappa) :: K \\
(\text{AtomBit}) \quad d &::= 0 \mid 1 \\
(\text{GMsg}) \quad o &::= \tau \mid e \mid \text{sw}
\end{aligned}$$

(a) The Preemptive Model

$$\begin{aligned}
\text{initRtMd}(\Pi, \mathbf{f}, \vec{v}, F) &\stackrel{\text{def}}{=} ((tl, \pi, F), \kappa), \\
&\text{if } (tl, \pi) \in \Pi \text{ and } tl.\text{InitCore}(\pi)(\mathbf{f})(\vec{v}) = \kappa \\
\text{initFList}(\sigma, FS) &\text{ iff } (\text{dom}(\sigma) \perp FS) \wedge \text{disjoint}(FS) \\
\frac{S \cap F = \emptyset \quad S \perp FS}{S \perp (F :: FS)} &\quad \frac{F \perp FS \quad \text{disjoint}(FS)}{\text{disjoint}(F :: FS)}
\end{aligned}$$

(b) Initialization

Figure 6. The Runtime Global Models

execute exactly one module. In the following presentation, we may not distinguish threads and modules.

Below we write $(\pi, F) \vdash \phi \xrightarrow{\tau}_{\delta} \phi'$ for multiple silent-step transitions, where δ is the accumulation of the footprints generated. $(\pi, F) \vdash \phi \xrightarrow{\tau}_{\delta}^* \phi'$ is for zero or multiple silent-step transitions, where δ is emp for the case of zero step. Similarly, for global steps, we write $W \xrightarrow{\tau}_{\delta}^+ W'$ for multiple silent-step transitions.

Besides, we also write $W \Rightarrow^+ W'$ for multiple steps that either are silent or produce sw events. It must contain at least one silent step. The meanings of $W \Rightarrow^+ \mathbf{abort}$ and $W \Rightarrow^+ \mathbf{done}$ are similar. $W \xrightarrow{e}_{\delta}^+ W'$ represents multiple steps with exactly one e event produced (where other steps either are silent or produce sw events).

Conventions. We usually write blackboard bold or capital letters (e.g., \mathbb{P} , \mathbb{W} , \mathbb{K} and Σ) for the notations at the source level to distinguish from the target-level ones (e.g., P , W , κ and σ). The set of modules at source is written as Γ , to distinguish from the target Π . Similarly, γ is a source module while π is a target one.

$$\begin{array}{c}
\frac{T = \{1 \rightsquigarrow K_1, \dots, n \rightsquigarrow K_n\} \quad \text{initFList}(\sigma, (F_1 :: \dots :: F_n :: FS))}{\forall i \in \{1, \dots, n\}. K_i = \text{initRtMd}(\Pi, \mathbf{f}_i, \epsilon, F_i) :: \epsilon \quad \mathbf{t} \in \{1, \dots, n\}} \text{Load} \quad \frac{\mathbf{t}' \in \text{dom}(T)}{((\Pi, T), (\mathbf{t}, 0, \sigma, FS)) \xrightarrow[\text{emp}]{\text{sw}} ((\Pi, T), (\mathbf{t}', 0, \sigma, FS))} \text{Switch} \\
\frac{T(\mathbf{t}) = ((tl, \pi, F), \kappa) :: K \quad (\pi, F) \vdash (\kappa, \sigma) \xrightarrow[\delta]{\tau} (\kappa', \sigma') \quad T' = T\{\mathbf{t} \rightsquigarrow ((tl, \pi, F), \kappa') :: K\}}{((\Pi, T), (\mathbf{t}, d, \sigma, FS)) \xrightarrow[\delta]{\tau} ((\Pi, T'), (\mathbf{t}, d, \sigma', FS))} \tau\text{-step} \quad \frac{T(\mathbf{t}) = ((tl, \pi, F), \kappa) :: K \quad (\pi, F) \vdash (\kappa, \sigma) \xrightarrow[\text{emp}]{\epsilon} (\kappa', \sigma) \quad T' = T\{\mathbf{t} \rightsquigarrow ((tl, \pi, F), \kappa') :: K\}}{((\Pi, T), (\mathbf{t}, 0, \sigma, FS)) \xrightarrow[\text{emp}]{\epsilon} ((\Pi, T'), (\mathbf{t}, 0, \sigma, FS))} \text{Print} \\
\frac{FS = F_1 :: FS' \quad \text{initRtMd}(\Pi, \mathbf{f}, \vec{v}, F_1) = ((tl_1, \pi_1, F_1), \kappa_1) \quad T' = \{\mathbf{t} \rightsquigarrow ((tl_1, \pi_1, F_1), \kappa_1) :: ((tl, \pi, F), \kappa') :: K\}}{((\Pi, T), (\mathbf{t}, 0, \sigma, FS)) \xrightarrow[\text{emp}]{\tau} ((\Pi, T'), (\mathbf{t}, 0, \sigma, FS'))} \text{EFCall} \\
\frac{T(\mathbf{t}) = ((tl_1, \pi_1, F_1), \kappa_1) :: ((tl, \pi, F), \kappa) :: K \quad (\pi_1, F_1) \vdash (\kappa_1, \sigma) \xrightarrow[\text{emp}]{\text{ret}(v)} (\kappa'_1, \sigma) \quad \text{AftExtn}(\kappa, v) = \kappa' \quad T' = T\{\mathbf{t} \rightsquigarrow ((tl, \pi, F), \kappa') :: K\}}{((\Pi, T), (\mathbf{t}, 0, \sigma, FS)) \xrightarrow[\text{emp}]{\tau} ((\Pi, T'), (\mathbf{t}, 0, \sigma, FS))} \text{EFRet} \\
\frac{T(\mathbf{t}) = ((tl, \pi, F), \kappa) :: K \quad (\pi, F) \vdash (\kappa, \sigma) \xrightarrow[\text{emp}]{\text{entA}} (\kappa', \sigma) \quad T' = T\{\mathbf{t} \rightsquigarrow ((tl, \pi, F), \kappa') :: K\}}{((\Pi, T), (\mathbf{t}, 0, \sigma, FS)) \xrightarrow[\text{emp}]{\tau} ((\Pi, T'), (\mathbf{t}, 1, \sigma, FS))} \text{EntAt} \quad \frac{T(\mathbf{t}) = ((tl, \pi, F), \kappa) :: K \quad (\pi, F) \vdash (\kappa, \sigma) \xrightarrow[\text{emp}]{\text{extA}} (\kappa', \sigma) \quad T' = T\{\mathbf{t} \rightsquigarrow ((tl, \pi, F), \kappa') :: K\}}{((\Pi, T), (\mathbf{t}, 1, \sigma, FS)) \xrightarrow[\text{emp}]{\tau} ((\Pi, T'), (\mathbf{t}, 0, \sigma, FS))} \text{ExtAt} \\
\frac{T(\mathbf{t}) = ((tl, \pi, F), \kappa) :: \epsilon \quad (\pi, F) \vdash (\kappa, \sigma) \xrightarrow[\text{emp}]{\text{ret}(v)} (\kappa', \sigma) \quad \mathbf{t}' \in \text{dom}(T \setminus \mathbf{t})}{((\Pi, T), (\mathbf{t}, 0, \sigma, FS)) \xrightarrow[\text{emp}]{\text{sw}} ((\Pi, T \setminus \mathbf{t}), (\mathbf{t}', 0, \sigma, FS))} \text{Term} \quad \frac{T(\mathbf{t}) = ((tl, \pi, F), \kappa) :: \epsilon \quad (\pi, F) \vdash (\kappa, \sigma) \xrightarrow[\text{emp}]{\text{ret}(v)} (\kappa', \sigma) \quad \text{dom}(T) = \{\mathbf{t}\}}{((\Pi, T), (\mathbf{t}, 0, \sigma, FS)) \xrightarrow[\text{emp}]{\tau} \text{done}} \text{Done}
\end{array}$$

Figure 7. The Preemptive Global Semantics

3.2 Event-Trace Refinement and Equivalence

$$\text{ProgEtr}((P, \sigma), \mathcal{B}) \text{ iff } \exists W. ((P, \sigma) \xrightarrow{\text{load}} W) \wedge \text{Etr}(W, \mathcal{B})$$

$$\frac{W \Rightarrow^+ \mathbf{abort}}{\text{Etr}(W, \mathbf{abort})} \quad \frac{W \xrightarrow{\epsilon}^+ W' \quad \text{Etr}(W', \mathcal{B})}{\text{Etr}(W, e :: \mathcal{B})}$$

$$\frac{W \Rightarrow^+ \mathbf{done}}{\text{Etr}(W, \mathbf{done})} \quad \frac{W \Rightarrow^+ W' \quad \text{Etr}(W', \epsilon)}{\text{Etr}(W, \epsilon)}$$

Figure 8. Definition of $\text{ProgEtr}((P, \sigma), \mathcal{B})$.

The correctness of compilation for concurrent programs is defined as the event-trace refinement or equivalence relations between source and target programs. An externally observable event trace \mathcal{B} is a finite or infinite sequence of external events e , and may end with a termination marker **done** or an abortion marker **abort**. It is co-inductively defined as follows.

$$(\text{EvtTrace}) \quad \mathcal{B} ::= \mathbf{done} \mid \mathbf{abort} \mid \epsilon \mid e :: \mathcal{B} \quad (\text{co-inductive})$$

Definition 3 (Event-Trace Refinement and Equivalence).

$$\begin{aligned} (\mathbb{P}, \Sigma) \sqsupseteq (P, \sigma) & \text{ iff} \\ & \forall \mathcal{B}. \text{ProgEtr}((P, \sigma), \mathcal{B}) \implies \text{ProgEtr}((\mathbb{P}, \Sigma), \mathcal{B}). \\ (\mathbb{P}, \Sigma) \approx (P, \sigma) & \text{ iff} \\ & \forall \mathcal{B}. \text{ProgEtr}((P, \sigma), \mathcal{B}) \iff \text{ProgEtr}((\mathbb{P}, \Sigma), \mathcal{B}). \\ \mathbb{P} \sqsupseteq_{ge, \varphi} P & \text{ iff} \\ & \forall \Sigma, \sigma. (ge \subseteq \text{locs}(\Sigma)) \wedge (\text{cl}(\text{dom}(\Sigma), \Sigma) = \text{dom}(\Sigma)) \wedge \\ & (\varphi\{\{\text{dom}(\Sigma)\}\} = \text{dom}(\sigma)) \wedge (\varphi\{\{\text{locs}(\Sigma)\}\} = \text{locs}(\sigma)) \wedge \\ & \text{Inv}(\varphi, \Sigma, \sigma) \implies (\mathbb{P}, \Sigma) \sqsupseteq (P, \sigma) \end{aligned}$$

4. The Non-Preemptive Semantics

A key step in our framework is to relate the source and target preemptive programs to the corresponding non-preemptive programs. In this section we define the global semantics of the non-preemptive programs, where a thread interacts with other threads at only synchronization points (i.e., when it enters and exits atomic blocks, and outputs). The non-preemptive semantics is the basis for both our new simulation (see Sec. 14) and our NPDRF definition (see Sec. 7).

To distinguish from the preemptive parallelism, we write **let** Π **in** $\mathbf{f}_1 \mid \dots \mid \mathbf{f}_n$ for the non-preemptive programs, denoted by \hat{P} . As shown in Fig. 6(b), the non-preemptive global world \widehat{W} is defined similarly to the preemptive world W , except that \widehat{W} records the atomic bits of all the threads (denoted by \mathfrak{d}). Unlike the preemptive semantics in Fig. 7(a) using the atomic bit d to know whether or not it is allowed to switch, here we need \mathfrak{d} to define NPDRF later in Sec. 7.

The non-preemptive global steps are formulated as $\widehat{W} \xrightarrow[\delta]{\alpha} \widehat{W}'$ (and $\widehat{W} \xrightarrow[\text{emp}]{\tau} \mathbf{abort}$ for the aborting steps, and $\widehat{W} \xrightarrow[\text{emp}]{\tau} \mathbf{done}$ for the terminating steps), defined in Fig. 7(b). The rules (Print_{np}), (EntAt_{np}) and (ExtAt_{np}) execute one step of the current thread \mathfrak{t} , and then non-deterministically switch to a thread \mathfrak{t}' (which could just be \mathfrak{t}). The corresponding global steps produce the sw events (or the external event e in the (Print_{np}) rule) which will be used to define NPDRF later. Other rules are very similar to their counterparts in the preemptive semantics in Fig. 7(a).

$$\begin{aligned} (\text{NPProg}) \quad \hat{P} & ::= \mathbf{let} \Pi \mathbf{in} \mathbf{f}_1 \mid \dots \mid \mathbf{f}_n \\ (\text{NPWorld}) \quad \widehat{W}, \widehat{W}' & ::= ((\Pi, T), (\mathfrak{t}, \mathfrak{d}, \sigma, FS)) \\ (\text{AtomBitSet}) \quad \mathfrak{d} & ::= \{\mathfrak{t}_1 \rightsquigarrow d_1, \dots, \mathfrak{t}_n \rightsquigarrow d_n\} \end{aligned}$$

Figure 9. The Non-preemptive Runtime Global Models

$$\begin{array}{c}
\frac{\text{initFList}(\sigma, (F_1 :: \dots :: F_n :: FS)) \quad \forall i \in \{1, \dots, n\}. K_i = \text{initRtMd}(\Pi, \mathbf{f}_i, \epsilon, F_i) :: \epsilon \\
T = \{\mathbf{t}_1 \rightsquigarrow K_1, \dots, \mathbf{t}_n \rightsquigarrow K_n\} \quad \mathbf{t} \in \{\mathbf{t}_1, \dots, \mathbf{t}_n\} \quad \text{dl} = \{\mathbf{t}_1 \rightsquigarrow 0, \dots, \mathbf{t}_n \rightsquigarrow 0\}}{\text{Load}_{\text{np}}} \\
\frac{(\text{let } \Pi \text{ in } \mathbf{f}_1 \mid \dots \mid \mathbf{f}_n, \sigma) \xrightarrow{\text{load}} ((\Pi, T), (\mathbf{t}, \text{dl}, \sigma, FS))}{\text{Load}_{\text{np}}} \\
\frac{T(\mathbf{t}) = ((\text{tl}, \pi, F), \kappa) :: K \quad (\pi, F) \vdash (\kappa, \sigma) \xrightarrow[\delta]{\tau} (\kappa', \sigma') \\
T' = T\{\mathbf{t} \rightsquigarrow ((\text{tl}, \pi, F), \kappa') :: K\}}{\text{tau-step}_{\text{np}}} \\
\frac{((\Pi, T), (\mathbf{t}, \text{dl}, \sigma, FS)) \xrightarrow[\delta]{\tau} ((\Pi, T'), (\mathbf{t}, \text{dl}, \sigma', FS))}{\text{tau-step}_{\text{np}}} \\
\frac{T(\mathbf{t}) = ((\text{tl}, \pi, F), \kappa) :: K \quad (\pi, F) \vdash (\kappa, \sigma) \xrightarrow[\text{emp}]{e} (\kappa', \sigma) \\
\text{dl}(\mathbf{t}) = 0 \quad T' = T\{\mathbf{t} \rightsquigarrow ((\text{tl}, \pi, F), \kappa') :: K\} \quad \mathbf{t}' \in \text{dom}(T)}{\text{Print}_{\text{np}}} \\
\frac{((\Pi, T), (\mathbf{t}, \text{dl}, \sigma, FS)) \xrightarrow[\text{emp}]{e} ((\Pi, T'), (\mathbf{t}', \text{dl}, \sigma, FS))}{\text{Print}_{\text{np}}} \\
\frac{T(\mathbf{t}) = ((\text{tl}, \pi, F), \kappa) :: K \quad (\pi, F) \vdash (\kappa, \sigma) \xrightarrow[\text{emp}]{\text{call}(\mathbf{f}, \vec{v})} (\kappa', \sigma) \quad \text{dl}(\mathbf{t}) = 0 \\
FS = F_1 :: FS' \quad \text{initRtMd}(\Pi, \mathbf{f}, \vec{v}, F_1) = ((\text{tl}_1, \pi_1, F_1), \kappa_1) \quad T' = \{\mathbf{t} \rightsquigarrow ((\text{tl}_1, \pi_1, F_1), \kappa_1) :: ((\text{tl}, \pi, F), \kappa') :: K\}}{\text{EFCall}_{\text{np}}} \\
\frac{((\Pi, T), (\mathbf{t}, \text{dl}, \sigma, FS)) \xrightarrow[\text{emp}]{\tau} ((\Pi, T'), (\mathbf{t}, \text{dl}, \sigma, FS'))}{\text{EFCall}_{\text{np}}} \\
\frac{T(\mathbf{t}) = ((\text{tl}_1, \pi_1, F_1), \kappa_1) :: ((\text{tl}, \pi, F), \kappa) :: K \quad (\pi_1, F_1) \vdash (\kappa_1, \sigma) \xrightarrow[\text{emp}]{\text{ret}(v)} (\kappa'_1, \sigma) \quad \text{dl}(\mathbf{t}) = 0 \\
\text{AftExtn}(\kappa, v) = \kappa' \quad T' = T\{\mathbf{t} \rightsquigarrow ((\text{tl}, \pi, F), \kappa') :: K\}}{\text{EFRet}_{\text{np}}} \\
\frac{((\Pi, T), (\mathbf{t}, \text{dl}, \sigma, FS)) \xrightarrow[\text{emp}]{\tau} ((\Pi, T'), (\mathbf{t}, \text{dl}, \sigma, FS))}{\text{EFRet}_{\text{np}}} \\
\frac{T(\mathbf{t}) = ((\text{tl}, \pi, F), \kappa) :: K \quad (\pi, F) \vdash (\kappa, \sigma) \xrightarrow[\text{emp}]{\text{entA}} (\kappa', \sigma) \\
\text{dl}(\mathbf{t}) = 0 \quad T' = T\{\mathbf{t} \rightsquigarrow ((\text{tl}, \pi, F), \kappa') :: K\} \quad \mathbf{t}' \in \text{dom}(T)}{\text{EntAt}_{\text{np}}} \\
\frac{((\Pi, T), (\mathbf{t}, \text{dl}, \sigma, FS)) \xrightarrow[\text{emp}]{\text{sw}} ((\Pi, T'), (\mathbf{t}', \text{dl}\{\mathbf{t} \rightsquigarrow 1\}, \sigma, FS))}{\text{EntAt}_{\text{np}}} \\
\frac{T(\mathbf{t}) = ((\text{tl}, \pi, F), \kappa) :: K \quad (\pi, F) \vdash (\kappa, \sigma) \xrightarrow[\text{emp}]{\text{extA}} (\kappa', \sigma) \\
\text{dl}(\mathbf{t}) = 1 \quad T' = T\{\mathbf{t} \rightsquigarrow ((\text{tl}, \pi, F), \kappa') :: K\} \quad \mathbf{t}' \in \text{dom}(T)}{\text{ExtAt}_{\text{np}}} \\
\frac{((\Pi, T), (\mathbf{t}, \text{dl}, \sigma, FS)) \xrightarrow[\text{emp}]{\text{sw}} ((\Pi, T'), (\mathbf{t}', \text{dl}\{\mathbf{t} \rightsquigarrow 0\}, \sigma, FS))}{\text{ExtAt}_{\text{np}}} \\
\frac{T(\mathbf{t}) = ((\text{tl}, \pi, F), \kappa) :: \epsilon \quad \text{dl}(\mathbf{t}) = 0 \\
(\pi, F) \vdash (\kappa, \sigma) \xrightarrow[\text{emp}]{\text{ret}(v)} (\kappa', \sigma) \quad \mathbf{t}' \in \text{dom}(T \setminus \mathbf{t})}{\text{Term}_{\text{np}}} \\
\frac{((\Pi, T), (\mathbf{t}, \text{dl}, \sigma, FS)) \xrightarrow[\text{emp}]{\text{sw}} ((\Pi, T \setminus \mathbf{t}), (\mathbf{t}', \text{dl} \setminus \mathbf{t}, \sigma, FS))}{\text{Term}_{\text{np}}} \\
\frac{T(\mathbf{t}) = ((\text{tl}, \pi, F), \kappa) :: \epsilon \quad \text{dl}(\mathbf{t}) = 0 \\
(\pi, F) \vdash (\kappa, \sigma) \xrightarrow[\text{emp}]{\text{ret}(v)} (\kappa', \sigma) \quad \text{dom}(T) = \{\mathbf{t}\}}{\text{Done}_{\text{np}}} \\
\frac{((\Pi, T), (\mathbf{t}, \text{dl}, \sigma, FS)) \xrightarrow[\text{emp}]{\tau} \text{done}}{\text{Done}_{\text{np}}}
\end{array}$$

Figure 10. The Non-Preemptive Global Semantics

5. The Footprint-Preserving Compositional Simulation (with EFCalls)

6. The Footprint-Preserving Compositional Simulation

In this section, we define a module-local simulation as the correctness obligation of each module's compilation, which is compositional and preserves footprints, allowing us to derive a whole-program simulation that preserves NPDRF. We will discuss compositionality in Sec. 6.2 and postpone the discussions of DRF and NPDRF preservation to Sec. 7.

6.1 Definition of the Module-Local Simulation

As informally explained in Sec. 2, the simulation establishes a consistency relation between executions of the source module γ and the target one π . To achieve compositionality, our simulation is also parameterized with rely/guarantee conditions, specifying the interactions between the current module and its environment at synchronization points. The consistency relation should be preserved under the environment transitions allowed in the rely condition.

Before explaining the simulation definition in Def. 28, we first define some key conditions in Fig. 19. Our simulation is parameterized with μ , defined as follows.

$$\mu \stackrel{\text{def}}{=} (\mathbb{S}, S, f),$$

where $\mathbb{S}, S \in \mathcal{P}(\text{BlockID})$ and $f \in \text{BlockID} \rightarrow \text{BlockID}$

Here S and \mathbb{S} specify the memory locations that are currently shared or were once shared. Keeping track of S and \mathbb{S} allows us to define the fixed specifications of rely/guarantee conditions in our simulation. The mapping f maps shared locations (including those once shared) at the source level to shared locations at the target. We require it to be an injective function, i.e., different source locations should be mapped to different target locations.

We require μ be well-formed with respect to the current thread's free spaces \mathbb{F} and F at the source and target levels. As defined in Fig. 19, $\text{wf}(\mu, \mathbb{F}, F)$ says that the injective function f in μ maps shared locations (in \mathbb{S}) to shared locations (in S). In particular, a location in \mathbb{F} that is shared (i.e., that has been exported to other threads) must be mapped to a shared location in F . Here $f\{\{\mathbb{S}\}\}$ (defined at the bottom of the figure) returns the set of target locations that are mapped from locations in \mathbb{S} . $f\{\{\mathbb{F} \cap \mathbb{S}\}\}$ is defined similarly.

$\text{FPmatch}(\Delta, \delta, \mu, F)$ relates the footprints Δ and δ at source and target levels. It says, every location accessed at the target (i.e., in δ) must either be from the current thread F , or correspond to a shared location at the source. For the latter case, the location must be accessed at the source (i.e., in Δ).

In our simulation μ may change after related steps at the source and target levels. Fig. 19 defines how μ is allowed to evolve to μ' under steps of the current thread (see EvolveG) and under the environment steps (see EvolveR). First, as defined in Evolve , the new μ' should be well-formed. The mapping f in μ' should relate the new states σ' and Σ' using Inv . Here Inv requires that the locations related by f be contained in the states, and the contents of these locations be also related (see \xrightarrow{f}). Besides, S and \mathbb{S} are evolved to reachable closures in the new states σ' and Σ' respectively. We define the closure function $\text{cl}(\mathbb{S}, \Sigma)$ at the bottom of the figure, which is specialized to the CompCert memory model to simplify the presentation. It returns all the locations reachable from \mathbb{S} . Since the CompCert memory model allows pointer arithmetics within blocks, we know all the locations in a reachable block (i.e., a block in which some location is reachable) are reachable. The last condition of Evolve says that f in the original μ should be preserved in the new μ' (here function subset is defined by viewing functions as special relations). EvolveG for the current thread's steps requires that the

$$\text{HFPG}(\Delta, (\mu, \mathbb{F})) \text{ iff } \text{FPG}(\Delta, \mathbb{F}, \mu.S)$$

$$\text{LFPG}(\delta, (\mu, \Delta, F)) \text{ iff } \text{FPG}(\delta, F, \mu.S) \wedge \text{FPmatch}(\mu, \Delta, \delta)$$

$$\text{FPmatch}(\mu, \Delta, \delta) \text{ iff } (\delta.rs \cap \llbracket \mu.S \rrbracket \subseteq \mu.f\langle\langle \Delta.rs \rangle\rangle) \wedge (\delta.ws \cap \llbracket \mu.S \rrbracket \subseteq \mu.f\langle\langle \Delta.ws \rangle\rangle)$$

$$\text{FPG}(\Delta, \mathbb{F}, \mathbb{S}) \text{ iff } \text{blocks}(\Delta.rs \cup \Delta.ws) \subseteq \mathbb{F} \cup \mathbb{S}$$

$$\text{HG}((\Delta, \Sigma'), (\mu, \mathbb{F})) \text{ iff } \text{G}(\Delta, \Sigma', \mathbb{F}, \mu.S)$$

$$\text{LG}((\delta, \sigma'), (\mu, \Delta, \Sigma', F)) \text{ iff } \text{G}(\delta, \sigma', F, \mu.S) \wedge \text{FPmatch}(\mu, \Delta, \delta) \wedge \text{Inv}(\mu.f, \Sigma', \sigma')$$

$$\text{G}(\Delta, \Sigma', \mathbb{F}, \mathbb{S}) \text{ iff } \text{FPG}(\Delta, \mathbb{F}, \mathbb{S}) \wedge (\mathbb{S} = \text{cl}(\mathbb{S}, \Sigma'))$$

$$\text{Rely}(\mu, (\Sigma, \Sigma', \mathbb{F}), (\sigma, \sigma', F)) \text{ iff } \text{R}(\Sigma, \Sigma', \mathbb{F}, \mu.S) \wedge \text{R}(\sigma, \sigma', F, \mu.S) \wedge \text{Inv}(\mu.f, \Sigma', \sigma')$$

$$\text{R}(\Sigma, \Sigma', \mathbb{F}, \mathbb{S}) \text{ iff } (\Sigma \stackrel{\llbracket \mathbb{F} \rrbracket}{=} \Sigma') \wedge (\mathbb{S} = \text{cl}(\mathbb{S}, \Sigma')) \wedge \text{forward}(\Sigma, \Sigma') \wedge ((\text{dom}(\Sigma') - \text{dom}(\Sigma)) \cap \mathbb{F} = \emptyset)$$

$$\text{Inv}(f, \Sigma, \sigma) \text{ iff } \forall b, n, b', n'. ((b, n) \in \text{locs}(\Sigma)) \wedge (f\langle(b, n)\rangle = (b', n')) \implies ((b', n') \in \text{locs}(\sigma)) \wedge (\Sigma(b)(n) \xrightarrow{f} \sigma(b')(n'))$$

$$v_1 \xrightarrow{f} v_2 \text{ iff } (v_1 \notin \mathbb{A}) \wedge (v_1 = v_2) \vee v_1 \in \mathbb{A} \wedge v_2 \in \mathbb{A} \wedge f\langle v_1 \rangle = v_2$$

$$\vec{v} \xrightarrow{f} \vec{v}' \text{ iff } \exists v_1, v'_1, \dots, v_n, v'_n. \vec{v} = \{v_1, \dots, v_n\} \wedge \vec{v}' = \{v'_1, \dots, v'_n\} \wedge \forall i. v_i \xrightarrow{f} v'_i$$

$$\iota \xrightarrow{f} \iota' \text{ iff } \iota = \iota' = e \vee \iota = \iota' = \text{entA} \vee \iota = \iota' = \text{extA} \vee \exists \mathbf{f}, \vec{v}, \vec{v}'. \iota = \text{call}(\mathbf{f}, \vec{v}) \wedge \iota' = \text{call}(\mathbf{f}, \vec{v}') \wedge \vec{v} \xrightarrow{f} \vec{v}' \vee \exists v, v'. \iota = \text{ret}(v) \wedge \iota' = \text{ret}(v') \wedge v \xrightarrow{f} v'$$

$$\text{Gargs}(\iota, \mathbb{S}) \text{ iff } \iota = e \vee \iota = \text{entA} \vee \iota = \text{extA} \vee \exists \mathbf{f}, \vec{v}. \iota = \text{call}(\mathbf{f}, \vec{v}) \wedge (\forall b, n. (b, n) \in \vec{v} \implies b \in \mathbb{S}) \vee \exists v. \iota = \text{ret}(v) \wedge (\forall b, n. v = (b, n) \implies b \in \mathbb{S})$$

$$\text{injective}(f) \text{ iff } \forall b_1, b_2, b'_1, b'_2. b_1 \neq b_2 \wedge f(b_1) = b'_1 \wedge f(b_2) = b'_2 \implies b'_1 \neq b'_2$$

$$f\{\{\mathbb{S}\}\} \stackrel{\text{def}}{=} \{b' \mid \exists b. (b \in \mathbb{S}) \wedge f(b) = b'\}$$

$$f\langle l \rangle \stackrel{\text{def}}{=} (b', n), \text{ if } l = (b, n) \wedge f(b) = b'$$

$$f\langle\langle ws \rangle\rangle \stackrel{\text{def}}{=} \{l' \mid \exists l. (l \in ws) \wedge f\langle l \rangle = l'\}$$

$$f|_{\mathbb{S}} \stackrel{\text{def}}{=} \{(b, f(b)) \mid b \in (\mathbb{S} \cap \text{dom}(f))\}$$

$$f_2 \circ f_1 \stackrel{\text{def}}{=} \{(b_1, b_3) \mid \exists b_2. f_1(b_1) = b_2 \wedge f_2(b_2) = b_3\}$$

$$\text{cl}(\mathbb{S}, \Sigma) \stackrel{\text{def}}{=} \bigcup_k \text{cl}_k(\mathbb{S}, \Sigma), \text{ where } \text{cl}_k(\mathbb{S}, \Sigma) \text{ is inductively defined:}$$

$$\text{cl}_0(\mathbb{S}, \Sigma) \stackrel{\text{def}}{=} \mathbb{S}$$

$$\text{cl}_{k+1}(\mathbb{S}, \Sigma) \stackrel{\text{def}}{=} \{b' \mid \exists b, n, n'. (b \in \text{cl}_k(\mathbb{S}, \Sigma)) \wedge \Sigma(b)(n) = (b', n')\}$$

Figure 11. Footprint Matching and Rely/Guarantee Conditions in Our Simulation

additional locations in $\mu'.f$ be allocated from the current thread's \mathbb{F} , while EvolveR for the environment steps says the opposite.

Definition 4 (Module-Local Downward Simulation).

$(sl, \gamma) \preceq_{ge, \varphi} (tl, \pi)$ iff
 $\text{dom}(sl.\text{InitCore}(\gamma)) = \text{dom}(tl.\text{InitCore}(\pi))$ and
for any $\mathbf{f}, \kappa, \mathbb{k}, \sigma, \Sigma, \mu, \mathbb{F}, F, \mathbb{S}$ and S , if $sl.\text{InitCore}(\gamma)(\mathbf{f}) = \mathbb{k}$,
 $tl.\text{InitCore}(\pi)(\mathbf{f}) = \kappa$, $ge \subseteq \text{locs}(\Sigma)$, $\text{dom}(\Sigma) = \mathbb{S} = \text{cl}(\mathbb{S}, \Sigma)$,
 $\text{dom}(\sigma) = S = \varphi\{\mathbb{S}\}$, $\text{locs}(\sigma) = \varphi\{\text{locs}(\Sigma)\}$, $\text{Inv}(\varphi, \Sigma, \sigma)$,
 $\mathbb{F} \cap \mathbb{S} = F \cap S = \emptyset$ and $\mu = (\mathbb{S}, S, \varphi|_{\mathbb{S}})$, then there exists $i \in \text{index}$
such that $((\gamma, \mathbb{F}), (\mathbb{k}, \Sigma), \bullet) \preceq_{\mu}^{(i, (\text{emp}, \text{emp}))} ((\pi, F), (\kappa, \sigma), \bullet)$.

Here we define $((\gamma, \mathbb{F}), (\mathbb{k}, \Sigma), \beta) \preceq_{\mu}^{(i, (\Delta_0, \delta_0))} ((\pi, F), (\kappa, \sigma), \beta)$
(where the bit β is either \circ or \bullet) as the largest relation such that
whenever $((\gamma, \mathbb{F}), (\mathbb{k}, \Sigma), \beta) \preceq_{\mu}^{(i, (\Delta_0, \delta_0))} ((\pi, F), (\kappa, \sigma), \beta)$, then
the following are true:

1. $\forall \mathbb{k}', \Sigma', \Delta$. if $\beta = \circ$, $(\gamma, \mathbb{F}) \vdash (\mathbb{k}, \Sigma) \xrightarrow{\tau}_{\Delta} (\mathbb{k}', \Sigma')$ and
 $\text{HFPG}(\Delta_0 \cup \Delta, (\mu, \mathbb{F}))$, then one of the following holds:
 - (a) there exists j such that
 - i. $j < i$, and
 - ii. $((\gamma, \mathbb{F}), (\mathbb{k}', \Sigma'), \circ) \preceq_{\mu}^{(j, (\Delta_0 \cup \Delta, \delta_0))} ((\pi, F), (\kappa, \sigma), \circ)$;
 - (b) or, there exist κ', σ', δ and j such that
 - i. $(\pi, F) \vdash (\kappa, \sigma) \xrightarrow{\tau}_{\delta}^+ (\kappa', \sigma')$, and
 - ii. $\text{LFPG}(\delta_0 \cup \delta, (\mu, \Delta_0 \cup \Delta, F))$, and
 - iii. $((\gamma, \mathbb{F}), (\mathbb{k}', \Sigma'), \circ) \preceq_{\mu}^{(j, (\Delta_0 \cup \Delta, \delta_0 \cup \delta))} ((\pi, F), (\kappa', \sigma'), \circ)$;
2. $\forall \mathbb{k}', \iota$. if $\beta = \circ$, $\iota \neq \tau$, $(\gamma, \mathbb{F}) \vdash (\mathbb{k}, \Sigma) \xrightarrow{\iota}_{\text{emp}} (\mathbb{k}', \Sigma)$ and
 $\text{HG}((\Delta_0, \Sigma), (\mu, \mathbb{F}))$ and $\text{Gargs}(\iota, \mu.\mathbb{S})$, then there exist κ', δ ,
 $\iota', \sigma', \kappa''$ and j such that
 - (a) $(\pi, F) \vdash (\kappa, \sigma) \xrightarrow{\tau}_{\delta}^* (\kappa', \sigma')$, and
 $(\pi, F) \vdash (\kappa', \sigma') \xrightarrow{\iota'}_{\text{emp}} (\kappa'', \sigma')$, and
 - (b) $\text{LG}((\delta_0 \cup \delta, \sigma'), (\mu, \Delta_0, \Sigma, F))$, and $\iota \xrightarrow{\mu, f} \iota'$, and
 - (c) $((\gamma, \mathbb{F}), (\mathbb{k}', \Sigma), \bullet) \preceq_{\mu}^{(j, (\text{emp}, \text{emp}))} ((\pi, F), (\kappa'', \sigma'), \bullet)$
or $\iota = \text{ret}(v)$;
3. $\forall \sigma', \Sigma', v_s, \mathbb{k}', v_t$, if $\beta = \bullet$ and $\text{AftExtn}(\mathbb{k}, v_s) = \mathbb{k}'$ and
 $v_s \xrightarrow{\mu, f} v_t \text{Rely}(\mu, (\Sigma, \Sigma', \mathbb{F}), (\sigma, \sigma', F))$,
then there exists κ', j such that
 - (a) $\text{AftExtn}(\kappa, v_t) = \kappa'$, and
 - (b) $((\gamma, \mathbb{F}), (\mathbb{k}', \Sigma'), \circ) \preceq_{\mu}^{(j, (\text{emp}, \text{emp}))} ((\pi, F), (\kappa', \sigma'), \circ)$.

The simulation $(sl, \gamma) \preceq (tl, \pi)$ relates the executions of the
source module γ and the target module π . We first use the languages'
 InitCore functions (see Fig. 4) to initialize the ‘‘core’’ states \mathbb{k} and
 κ . We assume their executions start from the same initial memory
states at the source and target levels, and the whole memory is shared
between the current thread and its environment. We define μ with
the injective function $\text{id}_{\mathbb{S}}$, which is the identity function on \mathbb{S} . That
is, $\text{dom}(\text{id}_{\mathbb{S}}) = \mathbb{S}$ and $\forall l \in \mathbb{S}. \text{id}_{\mathbb{S}}(l) = l$.

The index i and the history footprints (Δ_0, δ_0) are introduced to
ensure footprint preservation. Since compilations may reorder in-
structions, it may be more natural to relate the footprints of *multiple*
source steps to the corresponding target footprints. Our simulation
allows one to accumulate the footprints using the history footprints
 (Δ_0, δ_0) , and check FPmatch (the footprint matching condition,
defined in Fig. 19) later. Note that one may choose to check FPmatch
at every source step. Whether to accumulate footprints or check
 FPmatch depends on the compilation applications, and we leave
the choices to verifiers. However, for infinite executions, it is not
allowed to continuously accumulate the footprints and never check
 FPmatch . Therefore our simulation is parameterized with a metric
 i from a well-founded set index . The metric should decrease at

steps that accumulate the footprints and could be reset at steps that
check FPmatch .

We also introduce a bit β to indicate the synchronization points
(i.e., the program points when the control may switch to the
environment). It takes two values \bullet and \circ . Initially it is \bullet , indicating
a possible switch that allows the environment threads to make steps
before the current thread starts. Internal τ -steps of the current thread
keeps β to be \circ (see condition 1 in Def. 28). When the current thread
makes a transition labeled with e , entA or extA (see condition 2 in
Def. 28), we set β from \circ to \bullet . The environment can interfere with
the current thread when β is \bullet (see condition 3 in Def. 28). After
the environment interference, β should be reset to \circ .

Our simulation definition follows the diagram in Fig. 2(b). Every
source τ -step should correspond to zero-or-multiple target τ -steps
(see condition 1 in Def. 28). One may check FPmatch for footprints
accumulated till the current steps and reset the metric (see 1(a)),
or choose to continue accumulating the footprints and decrease the
metric (see 1(b)). To simplify the presentation, $[[[?]]]$ the footprints
should be accumulated when the source step correspond to zero
target steps.

At switch points (see condition 2 in Def. 28), we check FPmatch
for the accumulated footprints and reset the metric. We also evolve
 μ to μ' , which satisfies EvolveG (defined in Fig. 19).

We require the simulation relation is preserved after the envi-
ronment interference (see condition 3 in Def. 28). The environment
steps should not change the current thread's local memory. The con-
dition 3(a) disallow the environment threads to update locations in
the current thread's \mathbb{F} (or F at the target level) except in the shared
parts \mathbb{S} (or corresponds to \mathbb{S}). Here \equiv is defined at the bottom of
Fig. 4. After the environment step, μ is evolved to μ' satisfying
EvolveR (defined in Fig. 19).

6.2 Compositionality and the Non-Preemptive Global Simulation

Definition 5 (Whole-Program Downward Simulation).

$\hat{P} \preceq_{ge, \varphi} \hat{P}$ iff

there exist $\mathbf{f}_1, \dots, \mathbf{f}_n, \Gamma = \{(sl_1, \gamma_1), \dots, (sl_m, \gamma_m)\}$ and $\Pi = \{(tl_1, \pi_1), \dots, (tl_m, \pi_m)\}$, such that $\hat{P} = \mathbf{let} \Gamma \mathbf{in} \mathbf{f}_1 \mid \dots \mid \mathbf{f}_m$,

$\hat{P} = \mathbf{let} \Pi \mathbf{in} \mathbf{f}_1 \mid \dots \mid \mathbf{f}_m$,

$\forall i \in \{1, \dots, m\}. \text{dom}(sl_i.\text{InitCore}(\gamma_i)) = \text{dom}(tl_i.\text{InitCore}(\pi_i))$ and

for any $\Sigma, \sigma, \mu, \hat{W}$, if $ge \subseteq \text{locs}(\Sigma)$, $\text{dom}(\Sigma) = \mathbb{S} \subseteq \text{dom}(\varphi)$, $\text{cl}(\mathbb{S}, \Sigma) = \mathbb{S}$, $\text{locs}(\sigma) = \varphi\langle\langle \text{locs}(\Sigma) \rangle\rangle$, $\text{Inv}(\varphi, \Sigma, \sigma), \mu = (\mathbb{S}, \varphi\langle\langle \mathbb{S} \rangle\rangle, \varphi|_{\mathbb{S}})$,

and $(\hat{P}, \Sigma) \xrightarrow{\text{load}} \hat{W}$,

then there exists $\hat{W}, i \in \text{index}$ such that

$$(\hat{P}, \sigma) \xrightarrow{\text{load}} \hat{W} \text{ and } \hat{W} \preceq_{\mu}^{(i, (\text{emp}, \text{emp}))} \hat{W}.$$

Here we define $\hat{W} \preceq_{\mu}^{(i, (\Delta_0, \delta_0))} \hat{W}$ as the largest relation such that whenever $\hat{W} \preceq_{\mu}^{(i, (\Delta_0, \delta_0))} \hat{W}$, then the following are true:

1. $\hat{W}.t = \hat{W}.t$, and $\hat{W}.d = \hat{W}.d$, and $\text{wf}(\mu, \Sigma)$;
2. $\forall \hat{W}', \Delta$. if $\hat{W} \xrightarrow{\tau} \hat{W}'$, then one of the following holds:
 - (a) there exists j such that
 - i. $j < i$, and
 - ii. $\hat{W}' \preceq_{\mu}^{(j, (\Delta_0 \cup \Delta, \delta_0))} \hat{W}$;
 - (b) or, there exist \hat{W}', δ and j such that
 - i. $\hat{W} \xrightarrow{\tau} \hat{W}'$, and
 - ii. $\text{FPmatch}(\Delta_0 \cup \Delta, \delta_0 \cup \delta, \mu, ((\hat{W}.T)(\hat{W}.t)).\text{head}.F)$, and
 - iii. $\hat{W}' \preceq_{\mu}^{(j, (\Delta_0 \cup \Delta, \delta_0 \cup \delta))} \hat{W}'$;
3. $\forall T', d', \Sigma', o$. if $o \neq \tau$ and $\exists t'. \hat{W} \xrightarrow{\text{emp}} (T', t', d', \Sigma')$, then there exist $\hat{W}', \delta, \mu', T', \sigma'$ and j such that for any $t' \in \text{dom}(T')$, we have
 - (a) $\hat{W} \xrightarrow{\tau} \hat{W}'$, and $\hat{W}' \xrightarrow{\text{emp}} (T', t', d', \sigma')$, and
 - (b) $\text{FPmatch}(\Delta_0, \delta_0 \cup \delta, \mu, ((\hat{W}.T)(\hat{W}.t)).\text{head}.F)$, and
 - (c) $(T', t', d', \Sigma') \preceq_{\mu'}^{(j, (\text{emp}, \text{emp}))} (T', t', d', \sigma')$;
4. if $\hat{W} \xrightarrow{\text{emp}} \mathbf{done}$, then there exist \hat{W}' and δ such that
 - (a) $\hat{W} \xrightarrow{\tau} \hat{W}'$, and $\hat{W}' \xrightarrow{\text{emp}} \mathbf{done}$, and
 - (b) $\text{FPmatch}(\Delta_0, \delta_0 \cup \delta, \mu, ((\hat{W}.T)(\hat{W}.t)).\text{head}.F)$.

Lemma 6 (Compositionality, ④ in Fig. 3).

For any $\mathbf{f}_1, \dots, \mathbf{f}_n, ge_1, \dots, ge_m, ge, \varphi$,

$\Gamma = \{(sl_1, \gamma_1), \dots, (sl_m, \gamma_m)\}$, $\Pi = \{(tl_1, \pi_1), \dots, (tl_m, \pi_m)\}$ such that for any $i \in \{1, \dots, m\}$ we have $\text{wd}(sl_i)$ and $\text{wd}(tl_i)$, if

$$\forall i \in \{1, \dots, m\}. (sl_i, \gamma_i) \preceq_{ge_i, \varphi} (tl_i, \pi_i),$$

and $ge = \bigcup_i ge_i$, then

$$\mathbf{let} \Gamma \mathbf{in} \mathbf{f}_1 \mid \dots \mid \mathbf{f}_n \preceq_{ge, \varphi} \mathbf{let} \Pi \mathbf{in} \mathbf{f}_1 \mid \dots \mid \mathbf{f}_n.$$

6.3 Flip of the Non-Preemptive Global Simulation

Definition 7 (Whole-Program Upward Simulation).

$\hat{P} \preceq_{ge, \varphi} \hat{P}$ iff

there exist $\mathbf{f}_1, \dots, \mathbf{f}_n, \Gamma = \{(sl_1, \gamma_1), \dots, (sl_m, \gamma_m)\}$ and $\Pi = \{(tl_1, \pi_1), \dots, (tl_m, \pi_m)\}$, such that $\hat{P} = \mathbf{let} \Gamma \mathbf{in} \mathbf{f}_1 \mid \dots \mid \mathbf{f}_m$,

$\hat{P} = \mathbf{let} \Pi \mathbf{in} \mathbf{f}_1 \mid \dots \mid \mathbf{f}_m$,

$\forall i \in \{1, \dots, m\}. \text{dom}(sl_i.\text{InitCore}(\gamma_i)) = \text{dom}(tl_i.\text{InitCore}(\pi_i))$ and

for any $\Sigma, \sigma, \mu, \hat{W}$, if $ge \subseteq \text{locs}(\Sigma)$, $\text{dom}(\Sigma) = \mathbb{S} \subseteq \text{dom}(\varphi)$, $\text{cl}(\mathbb{S}, \Sigma) = \mathbb{S}$, $\text{locs}(\sigma) = \varphi\langle\langle \text{locs}(\Sigma) \rangle\rangle$, $\text{Inv}(\varphi, \Sigma, \sigma)$,

$\mu = (\mathbb{S}, \varphi\langle\langle \mathbb{S} \rangle\rangle, \varphi|_{\mathbb{S}})$, and $(\hat{P}, \sigma) \xrightarrow{\text{load}} \hat{W}$,

then there exists $\hat{W}, i \in \text{index}$ such that

$$(\hat{P}, \Sigma) \xrightarrow{\text{load}} \hat{W} \text{ and } \hat{W} \preceq_{\mu}^{(i, (\text{emp}, \text{emp}))} \hat{W}.$$

Here we define $\hat{W} \preceq_{\mu}^{(i, (\Delta_0, \delta_0))} \hat{W}$ as the largest relation such that whenever $\hat{W} \preceq_{\mu}^{(i, (\Delta_0, \delta_0))} \hat{W}$, then the following are true:

1. $\hat{W}.t = \hat{W}.t$, and $\hat{W}.d = \hat{W}.d$, and $\text{wf}(\mu, \Sigma)$;
2. $\forall \hat{W}', \delta$. if $\hat{W} \xrightarrow{\tau} \hat{W}'$, then one of the following holds:
 - (a) there exists j such that
 - i. $j < i$, and
 - ii. $\hat{W}' \preceq_{\mu}^{(j, (\Delta_0 \cup \Delta, \delta_0))} \hat{W}$;
 - (b) there exist \hat{W}', Δ and j such that
 - i. $\hat{W} \xrightarrow{\tau} \hat{W}'$, and
 - ii. $\text{FPmatch}(\Delta_0 \cup \Delta, \delta_0 \cup \delta, \mu, ((\hat{W}.T)(\hat{W}.t)).\text{head}.F)$, and
 - iii. $\hat{W}' \preceq_{\mu}^{(j, (\Delta_0 \cup \Delta, \delta_0 \cup \delta))} \hat{W}'$;
3. $\forall T', d', \sigma', o$. if $o \neq \tau$ and $\exists t'. \hat{W} \xrightarrow{\text{emp}} (T', t', d', \sigma')$, then there exist $\hat{W}', \Delta, \mu', T', \Sigma'$ and j such that for any $t' \in \text{dom}(T')$, we have
 - (a) $\hat{W} \xrightarrow{\tau} \hat{W}'$, and $\hat{W}' \xrightarrow{\text{emp}} (T', t', d', \Sigma')$, and
 - (b) $\text{FPmatch}(\Delta_0 \cup \Delta, \delta_0, \mu, ((\hat{W}.T)(\hat{W}.t)).\text{head}.F)$, and
 - (c) $(T', t', d', \sigma') \preceq_{\mu'}^{(j, (\text{emp}, \text{emp}))} (T', t', d', \Sigma')$;
4. if $\hat{W} \xrightarrow{\text{emp}} \mathbf{done}$, then there exist \hat{W}' and Δ such that
 - (a) $\hat{W} \xrightarrow{\tau} \hat{W}'$, and $\hat{W}' \xrightarrow{\text{emp}} \mathbf{done}$, and
 - (b) $\text{FPmatch}(\Delta_0 \cup \Delta, \delta_0, \mu, ((\hat{W}.T)(\hat{W}.t)).\text{head}.F)$.
5. $\neg(\hat{W} \xrightarrow{\text{emp}} \mathbf{abort})$ and $\neg(\hat{W} \xrightarrow{\text{emp}} \mathbf{abort})$.

Lemma 8 (Flip, ③ in Fig. 3).

For any $\mathbf{f}_1, \dots, \mathbf{f}_n, ge, \varphi, \Gamma = \{(sl_1, \gamma_1), \dots, (sl_m, \gamma_m)\}$,

$\Pi = \{(tl_1, \pi_1), \dots, (tl_m, \pi_m)\}$, if $\forall i. \text{det}(tl_i)$ and $\text{Safe}(\mathbf{let} \Gamma \mathbf{in} \mathbf{f}_1 \mid \dots \mid \mathbf{f}_m, ge)$

$$\mathbf{let} \Gamma \mathbf{in} \mathbf{f}_1 \mid \dots \mid \mathbf{f}_m \preceq_{ge, \varphi} \mathbf{let} \Pi \mathbf{in} \mathbf{f}_1 \mid \dots \mid \mathbf{f}_m,$$

then

$$\mathbf{let} \Pi \mathbf{in} \mathbf{f}_1 \mid \dots \mid \mathbf{f}_m \preceq_{ge, \varphi} \mathbf{let} \Gamma \mathbf{in} \mathbf{f}_1 \mid \dots \mid \mathbf{f}_m.$$

7. Data-Race-Freedom

$$\delta_1 \smile \delta_2 \quad \text{iff} \quad (\delta_1.ws \cap (\delta_2.rs \cup \delta_2.ws) = \emptyset) \wedge (\delta_1.rs \cap \delta_2.ws = \emptyset)$$

$$(\delta_1, d_1) \smile (\delta_2, d_2) \quad \text{iff} \quad (\delta_1 \smile \delta_2) \vee (d_1 = d_2 = 1)$$

$$\text{DRF}(P, \sigma) \quad \text{iff} \quad \neg((P, \sigma) \Longrightarrow \text{Race})$$

$$\text{DRF}(\mathbb{P}, ge) \quad \text{iff} \quad \forall \Sigma. (ge \subseteq \text{locs}(\Sigma)) \wedge (\text{cl}(\text{dom}(\Sigma), \Sigma) = \text{dom}(\Sigma)) \Longrightarrow \text{DRF}(\mathbb{P}, \Sigma)$$

$$\text{DRF}(\hat{P}, \sigma) \quad \text{iff} \quad \neg((\hat{P}, \sigma) \Longrightarrow \text{Race})$$

Lemma 9 (NPDRF Preservation, ⑦ in Fig. 3).

For any $\hat{\mathbb{P}}, \hat{P}, ge, \varphi, \Sigma$ and σ , if $\hat{P} \leq_{ge, \varphi} \hat{\mathbb{P}}, ge \subseteq \text{locs}(\Sigma), \text{cl}(\text{dom}(\Sigma), \Sigma) = \text{dom}(\Sigma), \varphi\{\text{dom}(\Sigma)\} = \text{dom}(\sigma), \varphi\{\text{locs}(\Sigma)\} = \text{locs}(\sigma), \text{Inv}(\varphi, \Sigma, \sigma)$ and $\text{NPDRF}(\hat{\mathbb{P}}, \Sigma)$, then $\text{NPDRF}(\hat{P}, \sigma)$.

Lemma 10 (Equivalence between DRF and NPDRF, ⑥ and ⑧ in Fig. 3).

For any $\mathbf{f}_1, \dots, \mathbf{f}_m, \sigma, \Pi = \{(t_1, \pi_1), \dots, (t_m, \pi_m)\}$ such that $\forall i. \text{wd}(t_i)$,

$$\text{DRF}(\mathbf{let} \Pi \mathbf{in} \mathbf{f}_1 \parallel \dots \parallel \mathbf{f}_m, \sigma) \iff \text{NPDRF}(\mathbf{let} \Pi \mathbf{in} \mathbf{f}_1 \mid \dots \mid \mathbf{f}_m, \sigma).$$

Lemma 11 (Equivalence between Preemptive and Non-Preemptive Programs, ① and ② in Fig. 3).

For any $\Pi, \mathbf{f}_1, \dots, \mathbf{f}_m, \sigma$, if $\text{DRF}(\mathbf{let} \Pi \mathbf{in} \mathbf{f}_1 \parallel \dots \parallel \mathbf{f}_m, \sigma)$, then

$$(\mathbf{let} \Pi \mathbf{in} \mathbf{f}_1 \mid \dots \mid \mathbf{f}_m, \sigma) \approx (\mathbf{let} \Pi \mathbf{in} \mathbf{f}_1 \parallel \dots \parallel \mathbf{f}_m, \sigma).$$

$$\begin{array}{c}
\frac{(P, \sigma) \xrightarrow{\text{load}} W \quad W \Longrightarrow^+ \text{Race}}{(P, \sigma) \Longrightarrow \text{Race}} \quad \frac{W \xrightarrow[\delta]{o} W'}{W \Longrightarrow W'} \text{Progress}}{\mathbf{t}_1 \neq \mathbf{t}_2 \quad \text{predict}(W, \mathbf{t}_1, (\delta_1, d_1)) \quad \text{predict}(W, \mathbf{t}_2, (\delta_2, d_2)) \quad \neg((\delta_1, d_1) \smile (\delta_2, d_2))}{W \Longrightarrow \text{Race}} \text{Race} \\
\\
\frac{W = ((\Pi, T), (-, 0, \sigma, FS)) \quad T(\mathbf{t}) = ((l, \pi, F), \kappa) :: K \quad (\pi, F) \vdash (\kappa, \sigma) \xrightarrow[\delta]{\tau} (\kappa', \sigma')}{\text{predict}(W, \mathbf{t}, (\delta, 0))} \text{Predict0} \\
\\
\frac{W = ((\Pi, T), (-, 0, \sigma, FS)) \quad T(\mathbf{t}) = ((l, \pi, F), \kappa) :: K \quad (\pi, F) \vdash (\kappa, \sigma) \xrightarrow[\text{emp}]{\text{entA}} (\kappa', \sigma) \quad (\pi, F) \vdash (\kappa', \sigma) \xrightarrow[\delta]{\tau}^* (\kappa'', \sigma'')}{\text{predict}(W, \mathbf{t}, (\delta, 1))} \text{Predict1}
\end{array}$$

(a) Preemptive Semantics

$$\begin{array}{c}
\frac{(\hat{P}, \sigma) \xrightarrow{\text{load}} \hat{W} \quad \mathbf{t}_1 \neq \mathbf{t}_2 \quad \text{NPpredict}(\hat{W}, \mathbf{t}_1, (\delta_1, d_1)) \quad \text{NPpredict}(\hat{W}, \mathbf{t}_2, (\delta_2, d_2)) \quad \neg((\delta_1, d_1) \smile (\delta_2, d_2))}{(\hat{P}, \sigma) \Longrightarrow \text{Race}} \\
\\
\frac{(\hat{P}, \sigma) \xrightarrow{\text{load}} \hat{W} \quad \hat{W} \Longrightarrow^+ \text{Race}}{(\hat{P}, \sigma) \Longrightarrow \text{Race}} \quad \frac{\hat{W} \xrightarrow[\delta]{o} \hat{W}'}{\hat{W} \Longrightarrow \hat{W}'} \text{Progress}_{\text{np}}}{\hat{W} \xrightarrow[\text{emp}]{o} \hat{W}' \quad o \neq \tau \quad \mathbf{t}_1 \neq \mathbf{t}_2 \quad \text{NPpredict}(\hat{W}', \mathbf{t}_1, (\delta_1, d_1)) \quad \text{NPpredict}(\hat{W}', \mathbf{t}_2, (\delta_2, d_2)) \quad \neg((\delta_1, d_1) \smile (\delta_2, d_2))}{\hat{W} \Longrightarrow \text{Race}} \text{Race}_{\text{np}} \\
\\
\frac{\hat{W} = ((\Pi, T), (-, d, \sigma, FS)) \quad ((\Pi, T), (\mathbf{t}, d, \sigma, FS)) \xrightarrow[\delta]{\tau}^* ((\Pi, T'), (\mathbf{t}, d, \sigma', FS)) \quad d(\mathbf{t}) = d}{\text{NPpredict}(\hat{W}, \mathbf{t}, (\delta, d))} \text{Predict}_{\text{np}}
\end{array}$$

(b) Non-Preemptive Semantics

Figure 12. Predictive Semantics for Defining Race

8. The Final Theorem

$\text{SeqComp} ::= (\text{CodeT}, \text{GE}, \varphi)$
 $\text{CodeT} \in \text{Module} \rightarrow \text{Module}$
 $\text{GE} \in \text{Module} \rightarrow \mathcal{P}(\mathcal{U})$
 $ge \in \mathcal{P}(\mathcal{U})$
 $\varphi \in \text{BlockID} \rightarrow \text{BlockID} \times \mathbb{N}$ where $\text{injective}(\varphi)$

Theorem 12 (Final Theorem).

For any $\text{SeqComp}_1, \dots, \text{SeqComp}_m$, sl_1, \dots, sl_m , tl_1, \dots, tl_m such that for any $i \in \{1, \dots, m\}$ we have $\text{wd}(sl_i)$, $\text{wd}(tl_i)$ and $\text{det}(tl_i)$, if

$$\forall i \in \{1, \dots, m\}. \text{Correct}(\text{SeqComp}_i, sl_i, tl_i),$$

then

$$\text{GCorrect}((\text{SeqComp}_1, sl_1, tl_1), \dots, (\text{SeqComp}_m, sl_m, tl_m)).$$

Definition 13 (Sequential Compiler Correctness).

$\text{Correct}(\text{SeqComp}, sl, tl)$ iff

$$\forall \gamma, \pi, ge. \text{SeqComp.CodeT}(\gamma) = \pi \wedge \text{SeqComp.GE}(\gamma) = ge \implies (sl, \gamma) \preceq_{ge, \text{SeqComp}, \varphi} (tl, \pi)$$

Definition 14 (Concurrent Compiler Correctness).

$\text{GCorrect}((\text{SeqComp}_1, sl_1, tl_1), \dots, (\text{SeqComp}_m, sl_m, tl_m))$ iff for any $\mathbf{f}_1, \dots, \mathbf{f}_n$, $\Gamma = \{(sl_1, \gamma_1), \dots, (sl_m, \gamma_m)\}$, $\Pi = \{(tl_1, \pi_1), \dots, (tl_m, \pi_m)\}$, for any ge_1, \dots, ge_m , ge, φ , if

1. $\forall i \in \{1, \dots, m\}. \text{SeqComp}_i.\text{CodeT}(\gamma_i) = \pi_i$,
2. $\forall i \in \{1, \dots, m\}. \text{SeqComp}_i.\text{GE}(\gamma_i) = ge_i$, $ge = \bigcup_i ge_i$,
 $\forall i \in \{1, \dots, m\}. \text{SeqComp}_i.\varphi = \varphi$,
3. $\text{DRF}(\mathbf{let} \Gamma \mathbf{in} \mathbf{f}_1 \parallel \dots \parallel \mathbf{f}_n, ge)$, and
 $\text{Safe}(\mathbf{let} \Gamma \mathbf{in} \mathbf{f}_1 \parallel \dots \parallel \mathbf{f}_n, ge)$,
4. $\forall i \in \{1, \dots, m\}. \text{ReachClose}(sl_i, \gamma_i, ge_i)$,

then

$$\mathbf{let} \Gamma \mathbf{in} \mathbf{f}_1 \parallel \dots \parallel \mathbf{f}_n \sqsupseteq_{ge, \varphi} \mathbf{let} \Pi \mathbf{in} \mathbf{f}_1 \parallel \dots \parallel \mathbf{f}_n.$$

Definition 15 (Safety). $\text{Safe}(\mathbb{W})$ iff $\neg \exists tr. \text{Etr}(\mathbb{W}, tr :: \text{abort})$.

$\text{Safe}(\mathbb{P}, \Sigma)$ iff $\forall \mathbb{W}. ((\mathbb{P}, \Sigma) \xrightarrow{\text{load}} \mathbb{W}) \implies \text{Safe}(\mathbb{W})$.

$\text{Safe}(\mathbf{let} \Gamma \mathbf{in} \mathbf{f}_1 \parallel \dots \parallel \mathbf{f}_m, ge)$ iff

$$\forall \Sigma. (ge \subseteq \text{locs}(\Sigma)) \wedge (\text{cl}(\text{dom}(\Sigma), \Sigma) = \text{dom}(\Sigma)) \implies \text{Safe}(\mathbf{let} \Gamma \mathbf{in} \mathbf{f}_1 \parallel \dots \parallel \mathbf{f}_m, \Sigma).$$

Definition 16. $\text{ReachClose}(sl, \gamma, ge)$ iff

$\forall \mathbb{k}, \mathbf{f}, \mathbb{F}, \mathbb{S}$. if

1. $sl.\text{InitCore}(\gamma, \mathbf{f}) = \mathbb{k}$,
2. $\text{blocks}(ge) \subseteq \mathbb{S}$,
3. $\mathbb{S} \cap \mathbb{F} = \emptyset$,

then $\text{RC}((\gamma, \mathbb{k}), (\mathbb{F}, \mathbb{S}))$.

Here RC is defined as the largest relation such that whenever $\text{RC}((\gamma, \mathbb{k}), (\mathbb{F}, \mathbb{S}))$, then the following holds:

$\forall \mathbb{k}', \Sigma, \Sigma', \iota, \Delta$. if $\mathbb{S} = \text{cl}(\mathbb{S}, \Sigma) \subseteq \text{dom}(\Sigma)$ and

$(\gamma, \mathbb{F}) \vdash (\mathbb{k}, \Sigma) \xrightarrow{\Delta} (\mathbb{k}', \Sigma')$, then $\text{G}(\Delta, \Sigma', \mathbb{F}, \mathbb{S})$ and $\text{Gargs}(\iota, \mathbb{S})$

and $\text{RC}((\gamma, \mathbb{k}'), (\mathbb{F}, \mathbb{S}))$.

Without EFCalls.

Definition 17 (Version I). $\text{ReachClose}_1(sl, \gamma, ge)$ iff $\forall \mathbb{k}, \mathbf{f}, \Sigma, \mathbb{F}, ac$. if

1. $sl.\text{InitCore}(\gamma, \mathbf{f}) = \mathbb{k}$,
2. $ge \subseteq \llbracket ac \rrbracket, ac = \text{cl}(ac, \Sigma) \subseteq \text{dom}(\Sigma)$,
3. $\text{dom}(\Sigma) \cap \mathbb{F} = \emptyset$,

then $\text{RC}_1((\gamma, \mathbb{F}), (\mathbb{k}, \Sigma), ac)$.

Here RC_1 is defined as the largest relation such that whenever $\text{RC}_1((\gamma, \mathbb{F}), (\mathbb{k}, \Sigma), ac)$, both the following are true:

1. $\forall \mathbb{k}', \Sigma', \Delta, ac'$. if $(\gamma, \mathbb{F}) \vdash (\mathbb{k}, \Sigma) \xrightarrow{\Delta} (\mathbb{k}', \Sigma')$ and $ac' = ac \cup (\text{dom}(\Sigma') - \text{dom}(\Sigma))$, then
 - (a) $\text{cl}(ac', \Sigma') = ac'$, and
 - (b) $\Delta \subseteq \llbracket ac' \rrbracket$, and
 - (c) $\text{RC}_1((\gamma, \mathbb{F}), (\mathbb{k}', \Sigma'), ac')$;
2. $\forall \mathbb{k}', \iota, \Sigma', ac'$. if $(\gamma, \mathbb{F}) \vdash (\mathbb{k}, \Sigma) \xrightarrow[\text{emp}]{} (\mathbb{k}', \Sigma)$, $\iota \neq \tau$ and $ac \subseteq ac' = \text{cl}(ac', \Sigma') \subseteq \text{dom}(\Sigma')$, then $\text{RC}_1((\gamma, \mathbb{F}), (\mathbb{k}', \Sigma'), ac')$.

Definition 18 (Version II). $\text{ReachClose}_2(sl, \gamma, ge)$ iff $\forall \mathbb{k}, \mathbf{f}, \Sigma, \mathbb{F}, ac$. if

1. $sl.\text{InitCore}(\gamma, \mathbf{f}) = \mathbb{k}$,
2. $ge \subseteq \llbracket ac \rrbracket, ac \subseteq \text{dom}(\Sigma), \text{cl}(\text{dom}(\Sigma), \Sigma) = \text{dom}(\Sigma)$,
3. $\text{dom}(\Sigma) \cap \mathbb{F} = \emptyset$,

then $\text{RC}_2((\gamma, \mathbb{F}), (\mathbb{k}, \Sigma), ac)$.

Here RC_2 is defined as the largest relation such that whenever $\text{RC}_2((\gamma, \mathbb{F}), (\mathbb{k}, \Sigma), ac)$, both the following are true:

1. $\forall \mathbb{k}', \Sigma', \Delta, ac'$. if $(\gamma, \mathbb{F}) \vdash (\mathbb{k}, \Sigma) \xrightarrow{\Delta} (\mathbb{k}', \Sigma')$ and $ac' = \text{cl}(ac \cup (\text{dom}(\Sigma') - \text{dom}(\Sigma)), \Sigma')$, then
 - (a) $\Delta \subseteq \llbracket ac' \rrbracket$, and
 - (b) $\text{RC}_2((\gamma, \mathbb{F}), (\mathbb{k}', \Sigma'), ac')$;
2. $\forall \mathbb{k}', \iota$. if $(\gamma, \mathbb{F}) \vdash (\mathbb{k}, \Sigma) \xrightarrow[\text{emp}]{} (\mathbb{k}', \Sigma)$ and $\iota \neq \tau$, then
 - (a) $\text{cl}(\text{dom}(\Sigma), \Sigma) = \text{dom}(\Sigma)$, and
 - (b) $\forall \Sigma', ac'$. if $\text{forward}(\Sigma, \Sigma'), \text{cl}(\text{dom}(\Sigma'), \Sigma') = \text{dom}(\Sigma')$ and $ac' = \text{cl}(ac, \Sigma')$, then $\text{RC}_2((\gamma, \mathbb{F}), (\mathbb{k}', \Sigma'), ac')$.

Definition 19 (Version III). $\text{ReachClose}_3(sl, \gamma, ge)$ iff $\forall \mathbb{k}, \mathbf{f}, \mathbb{F}, ac$. if

1. $sl.\text{InitCore}(\gamma, \mathbf{f}) = \mathbb{k}$,
2. $ge \subseteq \llbracket ac \rrbracket$,
3. $ac \cap \mathbb{F} = \emptyset$,

then $\text{RC}_3((\gamma, \mathbb{F}), \mathbb{k}, ac)$.

Here RC_3 is defined as the largest relation such that $\forall \gamma, \mathbb{F}, \mathbb{k}, ac$. if $\text{RC}_3((\gamma, \mathbb{F}), \mathbb{k}, ac)$, then $\forall \mathbb{k}', \Sigma, \Sigma', \iota, \Delta, ac'$. if $ac \subseteq \text{dom}(\Sigma), \text{cl}(ac, \Sigma) = ac$, $(\gamma, \mathbb{F}) \vdash (\mathbb{k}, \Sigma) \xrightarrow{\Delta} (\mathbb{k}', \Sigma')$ and $ac' = ac \cup (\text{dom}(\Sigma') - \text{dom}(\Sigma))$, then

1. $\Delta \subseteq \llbracket ac' \rrbracket$, and
2. $\text{cl}(ac', \Sigma') = ac'$, and
3. $\text{RC}_3((\gamma, \mathbb{F}), \mathbb{k}', ac')$.

Definition 20 (ReachClose in CompComp).

$\text{ReachClose}(sl, \gamma, ge)$ iff $\forall \mathbb{k}, \mathbf{f}, \Sigma, \mathbb{F}$. if

1. $sl.\text{InitCore}(\gamma, \mathbf{f}) = \mathbb{k}$,
2. $\text{dom}(\Sigma) \cap \mathbb{F} = \emptyset$,

then $\text{RC}((\gamma, \mathbb{F}), (\mathbb{k}, \Sigma), \emptyset)$.

Here RC is defined as the largest relation such that whenever $\text{RC}((\gamma, \mathbb{F}), (\mathbb{k}, \Sigma), ac)$, both the following are true:

1. $\forall \mathbb{k}', \Sigma', \Delta, ac'$. if $(\gamma, \mathbb{F}) \vdash (\mathbb{k}, \Sigma) \xrightarrow{\Delta} (\mathbb{k}', \Sigma')$ and $ac' = \text{cl}(\text{cl}(\text{getBlocks}(ge) \cup ac, \Sigma) \cup (\text{dom}(\Sigma') - \text{dom}(\Sigma)), \Sigma')$, then
 - (a) $\Delta \subseteq \llbracket \text{cl}(\text{getBlocks}(ge) \cup ac, \Sigma) \rrbracket$, and
 - (b) $\text{RC}((\gamma, \mathbb{F}), (\mathbb{k}', \Sigma'), ac')$;
2. $\forall \mathbb{k}', \iota, \Sigma'$. if $(\gamma, \mathbb{F}) \vdash (\mathbb{k}, \Sigma) \xrightarrow[\text{emp}]{} (\mathbb{k}', \Sigma)$ and $\iota \neq \tau$, then $\text{RC}((\gamma, \mathbb{F}), (\mathbb{k}', \Sigma'), ac)$.

Definition 21 (Nuclear in CompComp).

$\text{Nuclear}(sl, \gamma, ge)$ iff $\forall \mathbb{k}, \mathbf{f}, \Sigma, \mathbb{F}$. if

1. $sl.\text{InitCore}(\gamma, \mathbf{f}) = \mathbb{k}$,
2. $ge \subseteq \text{locs}(\Sigma), \text{cl}(\text{dom}(\Sigma), \Sigma) = \text{dom}(\Sigma)$,
3. $\text{dom}(\Sigma) \cap \mathbb{F} = \emptyset$,

then $\text{nuclear}((\gamma, \mathbb{F}), (\mathbb{k}, \Sigma))$.

Here nuclear is defined as the largest relation such that whenever $\text{nuclear}((\gamma, \mathbb{F}), (\mathbb{k}, \Sigma))$, both the following are true:

1. $\forall \mathbb{k}', \Sigma', \Delta$. if $(\gamma, \mathbb{F}) \vdash (\mathbb{k}, \Sigma) \xrightarrow{\Delta} (\mathbb{k}', \Sigma')$, then $\text{nuclear}((\gamma, \mathbb{F}), (\mathbb{k}', \Sigma'))$;
2. $\forall \mathbb{k}', \iota$. if $(\gamma, \mathbb{F}) \vdash (\mathbb{k}, \Sigma) \xrightarrow[\text{emp}]{} (\mathbb{k}', \Sigma)$ and $\iota \neq \tau$, then
 - (a) $\text{cl}(\text{dom}(\Sigma), \Sigma) = \text{dom}(\Sigma)$, and
 - (b) $\forall \Sigma'$. if $\text{forward}(\Sigma, \Sigma')$ and $\text{cl}(\text{dom}(\Sigma'), \Sigma') = \text{dom}(\Sigma')$, then $\text{nuclear}((\gamma, \mathbb{F}), (\mathbb{k}', \Sigma'))$.

9. Related Work and Conclusions

9.1 Compare with CompCertX

The CompCertX compiler is developed to support separate compilation of OS code. Taking the advantage of their push/pull memory model and log-based CPU-local machine, they are able to support parallel composition without major modification to CompCert. [\[\[\[HOW?\]\]\]](#)

But there are also limitations caused by the push/pull memory model:

- Program have to use push/pull primitives to synchronize between CPUs. E.g., given a properly implemented lock l , to get access to a shared variable x , the program has to be like the left one. Where in our approach, there is no need to introduce push/pull primitives, as shown on the right.

<pre>lock(l); pull(&x); write(&x, 1); push(&x); unlock(l);</pre>	<pre>lock(l); write(&x, 1); unlock(l);</pre>
------------------------------------------------------------------------------	--------------------------------------------------

[\[\[\[Why are we able to be simpler? No local copy?\]\]\]](#)

What's more, the push/pull primitives introduce redundant function calls, causing significant latency on their spin-lock implementations. For performance they removed push/pull primitives during "pretty-printing" phase, which is not verified. This is not an issue in our approach since we do not introduce unnecessary "null calls".

- There is no way to share a memory block unless it corresponds to some global identifier. Since the push/pull primitives are instantiated as CompCert external calls, their arguments must be blocks corresponding to some global identifier, as required by CompCert. Thus, for example, they are not able to implement a

general `malloc` function and share `malloced` memory among CPUs, e.g. appending to a shared linked list. In their approach, appending new node to a linked list is implemented by modifying a pre-allocated memory spaces (such as a globally declared array).

Of course they could not allow two CPUs simultaneously accessing different localtions in the same block, or simultaneously reading the same location, since they push/pull by block and each block is accessable by only one CPU.

References

- [1] X. Leroy. Formal verification of a realistic compiler. *Commun. ACM*, 52(7):107–115, 2009.
- [2] A. Lochbihler. Verifying a compiler for java threads. In *ESOP*, pages 427–447, 2010.
- [3] J. Ševčík, V. Vafeiadis, F. Z. Nardelli, S. Jagannathan, and P. Sewell. Relaxed-memory concurrency and verified compilation. In *POPL*, pages 43–54, 2011.
- [4] G. Stewart, L. Beringer, S. Cuellar, and A. W. Appel. Compositional compcert. In *POPL*, pages 275–287, 2015.

(HMod) $\gamma ::= \{f_1 \rightsquigarrow (\vec{x}_1, C_1), \dots, f_k \rightsquigarrow (\vec{x}_k, C_k)\}$

(HStmt) $C ::= \text{skip} \mid \mathbb{E} := \mathbb{E} \mid x := f(\vec{\mathbb{E}}) \mid \text{return}(\mathbb{E})$
 $\mid C; C \mid \text{if } (\mathbb{B}) C \text{ else } C \mid \text{while } (\mathbb{B})\{C\}$
 $\mid \text{sc_store}(\mathbb{E}, \mathbb{E}) \mid x := \text{sc_load}(\mathbb{E})$

(HEXpr) $\mathbb{E} ::= n \mid x \mid *E \mid \&E \mid E + E \mid E - E \mid E \times E$

(HBEp) $\mathbb{B} ::= \text{true} \mid \text{false} \mid E = E \mid E \leq E \mid \text{not } B \mid B \text{ and } B$

(HCore) $\mathbb{k} ::= C \mid k_1; k_2 \mid \dots$

$\text{InitCore}(\gamma)(f)(\vec{v}) \stackrel{\text{def}}{=} (\vec{x} := \vec{v}; C) \quad \text{if } \gamma(f) = (\vec{x}, C)$

$(\gamma, \mathbb{F}) \vdash (x := f(\vec{v}), \Sigma) \xrightarrow[\text{emp}]{\text{call}(f, \vec{v})} (x := f(\vec{v}), \Sigma) \quad \text{if } f \notin \text{dom}(\gamma)$

$(\gamma, \mathbb{F}) \vdash (x := f(\vec{v}), \Sigma) \xrightarrow[\text{emp}]{\text{recv}(v')} (x := v', \Sigma) \quad \text{if } f \notin \text{dom}(\gamma)$

$(\gamma, \mathbb{F}) \vdash (\text{return}(v'), \Sigma) \xrightarrow[\text{emp}]{\text{ret}(v')} (\text{skip}, \Sigma)$

$(\gamma, \mathbb{F}) \vdash (x := \text{sc_load}(src); \mathbb{k}) \xrightarrow[\text{emp}]{\text{call}(\text{sc_load}, src)} (x := \text{sc_load}(src); \mathbb{k})$

$(\gamma, \mathbb{F}) \vdash (\text{sc_store}(dst, v); \mathbb{k}) \xrightarrow[\text{emp}]{\text{call}(\text{sc_store}, dst::v)} (\text{sc_store}(dst, v); \mathbb{k})$

Figure 13. Source language: Clight + SC atomics

10. The Source Language: Clight + SC Atomics

The whole program $\text{let } \Gamma \cup \{(lang_{SC}, \gamma_{SC})\} \text{ in } f_1(\vec{v}_1) \parallel \dots \parallel f_n(\vec{v}_n)$: Here Γ is the collection of source modules written by the application programmers.

sc_store and **sc_load**: In the views of the application programmers, they are primitives that would generate external function calls.

To define the semantics of sc-primitives, we introduce a built-in module $(lang_{SC}, \gamma_{SC})$, where $lang_{SC}$ is defined in Fig.14. On a sc-primitive call, it first generate an entA event, then performs the corresponding memory operation; before returning the resulting value, it generate an extA event. The entA and extA events guarantees that the memory operation is exclusive to the environment.

(SCSyntax) $C ::= \text{Ent sc_op} \mid \text{sc_op} \mid \text{Ext sc_ret} \mid \text{sc_ret} \mid \text{END}$

(SCMod) $\gamma_{sc} ::= \{\text{sc_load} \rightsquigarrow C_1, \text{sc_store} \rightsquigarrow C_2\}$

(SCCore) $\mathbb{k}_{sc} ::= C$

(SCOp) $\text{sc_op} ::= \text{load}(src) \mid \text{store}(dst, v)$

(SCVar) $src, dst, v ::= \dots$

(SCRet) $\text{sc_ret} ::= \text{load_ret}(v) \mid \text{store_ret}$

$\text{InitCore}_{SC}(\gamma)(f)(\vec{v}) \stackrel{\text{def}}{=} \left\{ \begin{array}{ll} (\text{Ent } (\text{load}(src))) & \text{if } f = \text{sc_load} \wedge \vec{v} = src::nil \\ (\text{Ent } (\text{store}(dst, v))) & \text{if } f = \text{sc_store} \wedge \vec{v} = dst::v'::nil \end{array} \right.$

$(\gamma_{sc}, \mathbb{F}) \vdash (\text{Ent sc_op}, \Sigma) \xrightarrow{\text{entA}} (\text{Ent sc_op}, \Sigma)$

$(\gamma_{sc}, \mathbb{F}) \vdash (\text{Ent sc_op}, \Sigma) \xrightarrow[\text{emp}]{\text{recv}()} (\text{sc_op}, \Sigma)$

$\frac{\text{sc_op} = \text{load}(src) \quad \delta = (\{src\}, \emptyset) \quad \Sigma(src) = v \quad \text{sc_ret} = \text{load_ret}(v)}{(\gamma_{sc}, \mathbb{F}) \vdash (\text{sc_op}, \Sigma) \xrightarrow[\delta]{\tau} (\text{Ext sc_ret}, \Sigma)} \text{Load}$

$\frac{\text{sc_op} = \text{store}(dst, v) \quad \delta = (\emptyset, \{dst\}) \quad \Sigma' = \Sigma\{src \rightsquigarrow v\} \quad \text{sc_ret} = \text{store_ret}}{(\gamma_{sc}, \mathbb{F}) \vdash (\text{sc_op}, \Sigma) \xrightarrow[\delta]{\tau} (\text{Ext sc_ret}, \Sigma')} \text{Store}$

$(\gamma_{sc}, \mathbb{F}) \vdash (\text{Ext sc_ret}, \Sigma) \xrightarrow{\text{extA}} (\text{Ext sc_ret}, \Sigma)$

$(\gamma_{sc}, \mathbb{F}) \vdash (\text{Ext sc_ret}, \Sigma) \xrightarrow[\text{emp}]{\text{recv}()} (\text{sc_ret}, \Sigma)$

$\frac{\text{sc_ret} = \text{store_ret} \wedge \vec{v} = nil \quad \text{or} \quad \text{sc_ret} = \text{load_ret}(v') \wedge \vec{v} = v'::nil}{(\gamma_{sc}, \mathbb{F}) \vdash (\text{sc_ret}, \Sigma) \xrightarrow[\text{emp}]{\text{ret}(\vec{v})} (\text{END}, \Sigma)} \text{Return}$

$lang_{SC} \stackrel{\text{def}}{=} (SCMod, SCCore, \text{InitCore}_{SC}, \xrightarrow{\text{entA}}, \xrightarrow[\text{emp}]{\text{recv}()})$

Figure 14. SC_atomic lang

$(LMod) \quad \pi ::= \{f_1 \rightsquigarrow C_1, \dots, f_k \rightsquigarrow C_k\}$
 $(LCode) \quad C ::= \epsilon \mid c :: C$
 $(LInstr) \quad c ::= \begin{array}{l} \text{mov } r_d, r_s \mid \text{mov } r_d, v \mid \text{mov } l, r_s \\ \text{add } r_d, r_s \mid \dots \\ \text{call } f \mid \text{ret} \\ \text{lock xchg } l \ r_s \mid \text{lock xadd } l \ r_s \\ \text{lock cmpxchg } l \ r_s \end{array}$
 $(DynInstr) \quad \tilde{c} ::= \text{ENTER_AT} \mid \text{EXIT_AT}$
 $(DynCode) \quad \tilde{C} ::= \epsilon \mid c :: \tilde{C} \mid \tilde{c} :: \tilde{C}$
 $(Register) \quad r ::= r_0 \mid \dots \mid r_{31}$
 $(RegFile) \quad R \in Register \rightarrow Value$
 $(LCore) \quad \kappa ::= (\tilde{C}, R)$

$\text{InitCore}(\pi)(f)(\vec{v}) \stackrel{\text{def}}{=} (\pi(f), R) \quad \text{where } R = \text{????}$

$$\frac{f \notin \text{dom}(\pi) \quad R' = \text{????}}{(\pi, F) \vdash (((\text{call } f) :: \tilde{C}, R), \sigma) \xrightarrow[\text{emp}]{\text{call}(f)} (((\text{call } f) :: \tilde{C}, R'), \sigma)}$$

$$\frac{R' = \text{????}}{(\pi, F) \vdash ((\text{ret} :: \tilde{C}, R), \sigma) \xrightarrow[\text{emp}]{\text{ret}} ((\tilde{C}, R'), \sigma)}$$

$$\frac{}{(\pi, F) \vdash ((\text{lock mov } a_1 \ a_2) :: \tilde{C}) \xrightarrow[\text{emp}]{\tau} (\text{ENTER_AT} :: (\text{mov } a_1 \ a_2) :: \text{EXIT_AT} :: \tilde{C})}$$

$$(\pi, F) \vdash ((\text{ENTER_AT} :: C, R), \sigma) \xrightarrow[\text{emp}]{\text{entA}} ((C, R), \sigma)$$

$$(\pi, F) \vdash ((\text{EXIT_AT} :: C, R), \sigma) \xrightarrow[\text{emp}]{\text{extA}} ((C, R), \sigma)$$

$$\frac{f \notin \text{dom}(\gamma) \quad R' = \text{????}}{(\pi, F) \vdash (((\text{call } f) :: C, R), \Sigma) \xrightarrow[\text{emp}]{\text{recv}} ((C, R'), \Sigma)}$$

Figure 15. Pseudo-x86 Assembly

```

sc_store :
    mov   ecx   [esp + 8]
    mov   edx   [esp + 4]
    lock xchg  [edx] ecx
    ret

sc_load :
    xor   eax   eax
    mov   edx   [esp + 4]
    lock xadd  [edx] eax
    ret

sc_cas :
    mov   eax   [esp + 8]
    mov   eax   [eax]
    mov   edx   [esp + 4]
    mov   ecx   [esp + 12]
    lock cmpxchg [edx] ecx
    sete  al
    ret

```

Printing from pseudo-x86 to x86. Identical transformation.
[[[Have to study x86 code for function calls.]]]

11. The Target Language: x86 Assembly

The target programs written in the x86 assembly language are simply “printed” from programs in the “pseudo-x86” language defined in Fig. 15. The “pseudo-x86” language is an instantiation of our abstract language.

Pseudo-x86. As in the source language, ENTER_AT and EXIT_AT are used only as dynamic instructions.

Note that **sc_store** and **sc_load** are external function calls in the source code written by the application programmers. So they will be compiled to “call **sc_store**” and “call **sc_load**” at the pseudo-x86 level.

Recall that we have a built-in module for sc-primitives. Here we implement them in pseudo-x86:

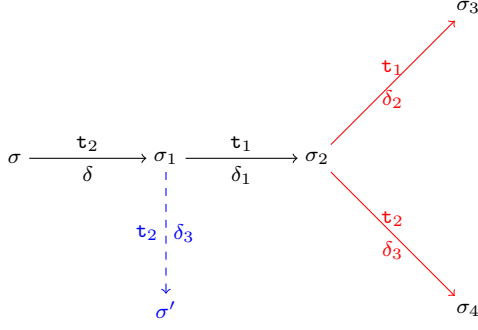


Figure 16. example

11.1 Why the (4)th condition was required?

The condition is required in proving Lemma 10, in particular, the " \Leftarrow " direction, i.e. $\text{DRF}(\hat{P}, \sigma) \implies \text{DRF}(P, \sigma)$. This could be proved by proving

$$((P, \sigma) \implies \text{Race}) \implies ((\hat{P}, \sigma) \implies \text{Race}).$$

Intuitively, the (4)th condition is necessary because we need to construct a race-condition under non-preemptive execution, based on some interleaving execution. In this case, we need all footprint generated during a interleaving execution is predictable from some state in a non-preemptive execution.

We illustrate this by an example.

By definition of $\implies \text{Race}$, we know

$$\exists W. (P, \sigma) \xrightarrow{\text{load}} W \wedge W \xrightarrow{\text{Race}}$$

Consider the special case shown in Fig.16

$$W = (T, 0, \mathbf{t}_2, \sigma)$$

$$(T, 0, \mathbf{t}_2, \sigma) \xrightarrow{\tau} (T_1, 0, \mathbf{t}_2, \sigma_1)$$

$$(T_1, 0, \mathbf{t}_1, \sigma_1) \xrightarrow{\tau} (T_2, 0, \mathbf{t}_1, \sigma_2)$$

$$(T_2, 0, \mathbf{t}_1, \sigma_2) \implies \text{Race}$$

where race is caused by

$$(T_2(\mathbf{t}_1), \sigma_2) \xrightarrow{\tau} (\kappa_1, \sigma_2) \quad \wedge \quad (T_2(\mathbf{t}_2), \sigma_2) \xrightarrow{\tau} (\kappa_2, \sigma_3)$$

and $\neg\delta_2 \smile \delta_3$.

By definition we are able to construct the corresponding non-preemptive state $\hat{W} = (T, \vec{0}, \mathbf{t}_2, \sigma)$ such that

$$(\hat{P}, \sigma) \xrightarrow{\text{load}} \hat{W} \xrightarrow{\tau} (T_1, \vec{0}, \mathbf{t}_2, \sigma_1)$$

, and we have to show $(T_1, \vec{0}, \mathbf{t}_2, \sigma_1) \implies \text{Race}$, since we are not able to reach σ_2 in a non-preemptive execution.

We consider two cases:

Case 1: $(T_1, 0, \mathbf{t}_2, \sigma_1) \implies \text{Race}$

we could derive $(T_1, \vec{0}, \mathbf{t}_2, \sigma_1) \implies \text{Race}$ trivially

Case 2: $\neg(T_1, 0, \mathbf{t}_2, \sigma_1) \implies \text{Race}$

It's trivial to see

$$\text{NPpredict}((T_1, \vec{0}, \mathbf{t}_2, \sigma_1), \mathbf{t}_1, (\delta_1 \cup \delta_2, 0))$$

The difficulty is to predict $(\delta_3, 0)$ for thread \mathbf{t}_2 under $(T_1, \vec{0}, \mathbf{t}_2, \sigma_1)$.

Now we could apply the (4)th condition and $\neg(T_1, 0, \mathbf{t}_2, \sigma_1) \implies \text{Race}$ to prove $\text{NPpredict}((T_1, \vec{0}, \mathbf{t}_2, \sigma_1), \mathbf{t}_2, (\delta_3, 0))$

Let $\delta_0 = \bigcup \{\delta' \mid \exists \kappa', \sigma'. (T(\mathbf{t}_2), \sigma_1) \xrightarrow{\tau} (\kappa', \sigma')\}$.

By $\neg(T_1, 0, \mathbf{t}_2, \sigma_1) \implies \text{Race}$, we know

$\sigma_1 \xrightarrow{\delta_0, \text{rs}} \sigma_2$ and $\text{locs}(\sigma_1) \cap \delta_0, \text{ws} = \text{locs}(\sigma_2) \cap \delta_0, \text{ws}$.

Then by the (4)th condition, we have

$\exists \kappa'', \sigma''. (T(\mathbf{t}_2), \sigma_1) \xrightarrow{\tau} (\kappa'', \sigma'')$

[[[Is the condition necessary?]]]

We are able to give a language satisfying (1)-(3) but does not satisfy (4), and in this case $\text{DRF}(\hat{P}, \sigma) \implies \text{DRF}(P, \sigma)$ is not provable.

Assume we have this kind of statement " $C_1 \hat{C}_2$ ", meaning we non-deterministically choose one statement that is able to progress, and execute.

Assume we have 2 global variables x and y that are initialized to 0.

$$\frac{(C_1, \sigma) \xrightarrow{\tau} (\text{skip}, \sigma')}{(C_1 \hat{C}_2, \sigma) \xrightarrow{\tau} (\text{skip}, \sigma')} \text{Nondet.1}$$

$$\frac{(C_2, \sigma) \xrightarrow{\tau} (\text{skip}, \sigma')}{(C_1 \hat{C}_2, \sigma) \xrightarrow{\tau} (\text{skip}, \sigma')} \text{Nondet.2}$$

$$\frac{[[E]]_\sigma = \text{true} \wedge (C, \sigma) \xrightarrow{\tau} (\text{skip}, \sigma')}{(\text{with}(E)\text{do}(C), \sigma) \xrightarrow{\tau} (\text{skip}, \sigma')} \text{Withdo}$$

$$\begin{array}{l} \mathbf{t}_1 : \\ \text{with}(x == 1)\text{do } y := 1 \hat{\text{nop}}; \\ \text{with}(y == 1)\text{do } y := 2 \hat{\text{nop}}; \end{array} \quad \left| \quad \begin{array}{l} \mathbf{t}_2 : \\ x := 1; \\ \text{with}(y == 1)\text{do } y := 3 \hat{\text{nop}}; \end{array} \right.$$

Figure 17. Wierd counter example

All non-preemptive executions are data-race free (according to our definition of NPDRF), but the preemptive execution would result in data race.

And it is obvious that this language satisfied condition (1)-(3) while failed to satisfy condition (4).

11.2 Alternative Well-Defined Definition

$$\sigma_1 \perp \sigma_2 \stackrel{\text{def}}{=} \text{locs}(\sigma_1) \cap \text{locs}(\sigma_2) = \emptyset$$

$$\sigma_1 \cdot \sigma_2 \stackrel{\text{def}}{=} \begin{cases} \{(b, \sigma_1(b) \uplus \sigma_2(b)) \mid b \in \text{dom}(\sigma_1) \cup \text{dom}(\sigma_2)\} \\ \text{Undefined} \end{cases}$$

Definition 22 (Well-Defined Languages (Alternative)). $\text{wd}'(tl)$ iff for any π, F, κ, σ ,

(1) [Forward]

[[[Is this necessary? where was it used?]]]

$\forall \kappa', \sigma', \iota, \delta$, if $(\pi, F) \vdash (\kappa, \sigma) \xrightarrow[\delta]{\iota} (\kappa', \sigma')$ then $\text{forward}(\sigma, \sigma')$ and $\text{dom}(\sigma') - \text{dom}(\sigma) \subseteq F$

(2) [Alloc/Free is captured by write set, contents of location outside write set stay unchanged]

[[[Could some part of this property derived from frame?]]]

[[[Why do we need locs captured by δ ?]]]

$\forall \kappa', \sigma', \iota, \delta$. if $(\pi, F) \vdash (\kappa, \sigma) \xrightarrow[\delta]{\iota} (\kappa', \sigma')$ then

$(\text{locs}(\sigma') - \text{locs}(\sigma)) \cup (\text{locs}(\sigma) - \text{locs}(\sigma')) \subseteq \delta.\text{ws}$ and

$$\sigma \xrightarrow[\delta]{\text{locs}(\sigma) - \delta.\text{ws}} \sigma'$$

(i.e. $\exists \sigma_x, \sigma_y, \sigma'_y. \sigma = \sigma_x \cdot \sigma_y \wedge \sigma' = \sigma_x \cdot \sigma'_y$ and $\text{locs}(\sigma_x) = \text{locs}(\sigma) - \delta.\text{ws}$)

(3) [Frame Property (instrumented with footprint)]

$\forall \sigma_1, \sigma_2, \kappa', \sigma', \iota, \delta$. if $\sigma_1 \perp \sigma_2$, $\sigma = \sigma_1 \cdot \sigma_2$, and $(\pi, F) \vdash (\kappa, \sigma) \xrightarrow[\delta]{\iota} (\kappa', \sigma')$, then either $(\pi, F) \vdash (\kappa, \sigma_1) \xrightarrow[\delta]{\iota} \mathbf{abort}$

or $\exists \sigma'_1. \sigma' = \sigma'_1 \cdot \sigma_2 \wedge (\pi, F) \vdash (\kappa, \sigma_1) \xrightarrow[\delta]{\iota} (\kappa', \sigma'_1)$

(4) [Safety Monotonicity (instrumented with footprint and freelist)]

[[[It is different from the original safety monotonicity]]]

[[[for deterministic allocation, we need σ_2 orthogonal to freelist]]]

$\forall \sigma_1, \sigma_2$, if $\sigma = \sigma_1 \cdot \sigma_2$, $(\text{dom}(\sigma_2) - \text{dom}(\sigma_1)) \cap F = \emptyset$ then

$\forall \iota, \delta, \kappa', \sigma'_1. (\pi, F) \vdash (\kappa, \sigma_1) \xrightarrow[\delta]{\iota} (\kappa', \sigma'_1)$

$\implies (\pi, F) \vdash (\kappa, \sigma_1 \cdot \sigma_2) \xrightarrow[\delta]{\iota} (\kappa', \sigma'_1 \cdot \sigma_2)$

(5) [Footprint captures enough resource for safe execution]

$\forall \sigma_1, \sigma_2, \delta_0$. if $\neg((\pi, F) \vdash (\kappa, \sigma) \mapsto \mathbf{abort})$,

$\delta_0 = \bigcup \{\delta \mid \exists \kappa', \sigma', \iota. (\pi, F) \vdash (\kappa, \sigma) \xrightarrow[\delta]{\iota} (\kappa', \sigma')\}$,

$\sigma = \sigma_1 \cdot \sigma_2$, and $\text{locs}(\sigma_2) \cap \delta_0 = \emptyset$, then

$\neg((\pi, F) \vdash (\kappa, \sigma_1) \mapsto \mathbf{abort})$

(x) if $\iota = \text{call}(\mathbf{f}, \vec{v})$, then $\forall v'. \exists \kappa''. \text{AftExtn}(\kappa', v') = \kappa''$;
if $\iota = \text{entA} \vee \iota = \text{extA}$, then $\forall v'. \text{AftExtn}(\kappa', v') = \kappa'$.

The alternative condition wd' is not able to imply the original wd .
But might be sufficient for the proof of DRF equivalence lemmas.

Recall the condition needed for DRF equivalence proof:

Definition 23 (Condition A_0). $A_0(tl)$ if and only if

$\forall \sigma_0, \pi_1, F_1, \kappa_1, \iota_1, \delta_1, \sigma_1, \kappa'_1, \pi_2, F_2, \kappa_2$. if

1. $(\pi_1, F_1) \vdash (\kappa_1, \sigma_0) \xrightarrow[\delta_1]{\iota_1} (\kappa'_1, \sigma_1)$, and

2. $\neg((\pi_2, F_2) \vdash (\kappa_2, \sigma_0) \mapsto \mathbf{abort})$ and

$\forall \iota'_1, \kappa'_2, \sigma'_1, \delta'_1$.

$(\pi_2, F_2) \vdash (\kappa_2, \sigma_0) \xrightarrow[\delta'_1]{\iota'_1} (\kappa'_2, \sigma'_1) \implies \delta'_1 \sim \delta_1$

3. $F_1 \cap F_2 = \emptyset$

then $\forall \iota_2, \kappa'_2, \delta_2$.

$\exists \sigma_2. (\pi_2, F_2) \vdash (\kappa_2, \sigma_0) \xrightarrow[\delta_2]{\iota_2} (\kappa'_2, \sigma_2)$

$\Leftrightarrow \exists \sigma'_2. (\pi_2, F_2) \vdash (\kappa_2, \sigma_1) \xrightarrow[\delta_2]{\iota_2} (\kappa'_2, \sigma'_2)$.

Lemma 24. $\forall tl. \text{wd}'(tl) \implies A_0(tl)$.
, otherwise

Proof. By assumption (1) of A_0 and condition (2) of $\text{wd}'(tl)$, we have $\sigma_0 \xrightarrow[\text{locs}(\sigma_0) - \delta_1.\text{ws}}{\delta_1} \sigma_1$.

i.e. $\exists \sigma_x, \sigma_y, \sigma'_y. \sigma_0 = \sigma_x \cdot \sigma_y$ and $\sigma_1 = \sigma_x \cdot \sigma'_y$ and $\text{locs}(\sigma_y) \subseteq \delta_1.\text{ws}$ and $\text{locs}(\sigma'_y) \subseteq \delta_1.\text{ws}$ and $\text{dom}(\sigma_y) = \text{dom}(\sigma_x)$

Let $\delta_0 = \bigcup \{\delta' \mid \exists \kappa', \sigma', \iota'. (\pi_2, F_2) \vdash (\kappa_2, \sigma_0) \xrightarrow[\delta']{\iota'} (\kappa', \sigma')\}$

By assumption (2) of A_0 ,

$\text{locs}(\sigma_y) \cap \delta_0 = \text{locs}(\sigma'_y) \cap \delta_0 \subseteq \delta_1.\text{ws} \cap \delta_0 = \emptyset$

By wd' (5), and assumption 2. of $A_0(tl)$,

$\neg((\pi_2, F_2) \vdash (\kappa_2, \sigma_x) \mapsto \mathbf{abort})$

Now we prove one direction (\Leftarrow) of the goal, i.e.

$\exists \sigma'_2. (\pi_2, F_2) \vdash (\kappa_2, \sigma_1) \xrightarrow[\delta_2]{\iota_2} (\kappa'_2, \sigma'_2)$

$\Rightarrow \exists \sigma_2. (\pi_2, F_2) \vdash (\kappa_2, \sigma_0) \xrightarrow[\delta_2]{\iota_2} (\kappa'_2, \sigma_2)$.

The assumption could be rewritten as

$(\pi_2, F_2) \vdash (\kappa_2, \sigma_x \cdot \sigma'_y) \xrightarrow[\delta_2]{\iota_2} (\kappa'_2, \sigma'_2)$.

By frame property, exists $\sigma'_x, (\pi_2, F_2) \vdash (\kappa_2, \sigma_x) \xrightarrow[\delta_2]{\iota_2} (\kappa'_2, \sigma'_x)$

By safety monotonicity, (2) of $\text{wd}'(tl)$, and assumption 3. of A_0 ,

$(\pi_2, F_2) \vdash (\kappa_2, \sigma_x \cdot \sigma_y) \xrightarrow[\delta_2]{\iota_2} (\kappa'_2, \sigma'_x \cdot \sigma_y)$, i.e. $\exists \sigma_2. (\pi_2, F_2) \vdash$

$(\kappa_2, \sigma_0) \xrightarrow[\delta_2]{\iota_2} (\kappa'_2, \sigma_2)$

□

11.3 Problems with wd'

wd' suffers from these problems

- Properties of language and footprints are mixed up. Condition (1), (3), (4) are intended to describe properties of language, while condition (2), (5) describes properties of δ .
- Condition (3) requires the footprint of two steps are the same, while it is not intended to describe property of δ .
- Similarly, condition (4) requires footprint and resulting state of the two steps are the same. It is different from the original safety monotonicity and it is not clear why such property is necessary.
- While we have read set and write set in footprint, no condition describes property of read set

To deal with these problems, I made the following modifications on wd':

- reorganized the wd' definition, define properties of languages and footprints separately.
- removed requirement of same δ , instead we require read set and write set should be subset of $\text{locs}(\sigma) \cup \llbracket F - \text{dom}(\sigma) \rrbracket$
- removed requirements on result states in condition (4), now (4) is almost the same as original safety monotonicity
- restored condition (3) of wd, describes that the execution actually depends on memory contents in read set and memory domain in write set.

The resulting "well-defined" property are defined separately as "well-defined language" and "well-defined footprint":

$$\text{LEqPre}(\sigma_1, \sigma_2, \delta, F) \stackrel{\text{def}}{=} \sigma_1 \xrightarrow{\delta, rs} \sigma_2 \wedge (\text{locs}(\sigma_1) \triangle \text{locs}(\sigma_2)) \cap \delta.wS = \emptyset \wedge (\text{dom}(\sigma_1) \triangle \text{dom}(\sigma_2)) \cap F = \emptyset$$

$$\text{LEqPost}(\sigma'_1, \sigma'_2, \delta, F) \stackrel{\text{def}}{=} \sigma'_1 \xrightarrow{\delta, ws} \sigma'_2 \wedge (\text{dom}(\sigma'_1) \triangle \text{dom}(\sigma'_2)) \cap F = \emptyset$$

$$\text{LEffect}(\sigma_1, \sigma_2, \delta, F) \stackrel{\text{def}}{=} \sigma_1 \xrightarrow{\text{locs}(\sigma_1) - \delta.ws} \sigma_2 \wedge (\text{locs}(\sigma_1) \triangle \text{locs}(\sigma_2)) \subseteq \delta.wS \wedge (\text{dom}(\sigma_1) \triangle \text{dom}(\sigma_2)) \subseteq F$$

Definition 25 (wd-lang(tl)). For any language tl , wd-lang(tl) iff for any π, F, κ, σ ,

1. *Forward*
 $\forall \kappa', \sigma', \iota, \delta$, if $(\pi, F) \vdash (\kappa, \sigma) \xrightarrow{\iota, \delta} (\kappa', \sigma')$ then forward(σ, σ')
2. *Frame Property*
 $\forall \sigma_1, \sigma_2, \kappa', \sigma', \iota, \delta$. if $\sigma_1 \perp \sigma_2$, $\sigma = \sigma_1 \cdot \sigma_2$, and $(\pi, F) \vdash (\kappa, \sigma) \xrightarrow{\iota, \delta} (\kappa', \sigma')$, then either $(\pi, F) \vdash (\kappa, \sigma_1) \mapsto \mathbf{abort}$ or $\exists \sigma'_1, \delta'. \sigma' = \sigma'_1 \cdot \sigma_2 \wedge (\pi, F) \vdash (\kappa, \sigma_1) \xrightarrow{\iota, \delta'} (\kappa', \sigma'_1)$
3. *Safety Monotonicity*
[[[This condition is redundant for DRF equivalence proof]]]
 $\forall \sigma_1, \sigma_2$. if $\sigma = \sigma_1 \cdot \sigma_2$, $(\text{dom}(\sigma_2) - \text{dom}(\sigma_1)) \cap F = \emptyset$ then $\neg((\pi, F) \vdash (\kappa, \sigma_1) \mapsto \mathbf{abort}) \implies \neg((\pi, F) \vdash (\kappa, \sigma) \mapsto \mathbf{abort})$
- x. if $\iota = \text{call}(f, \vec{v})$, then $\forall v'. \exists \kappa''. \text{AftExtn}(\kappa', v') = \kappa''$;
if $\iota = \text{entA} \vee \iota = \text{extA}$, then $\forall v'. \text{AftExtn}(\kappa', v') = \kappa'$.

Definition 26 (wd-fp(tl)). For any language tl , wd-fp(tl) iff for any π, F, κ ,

1. *Footprint should not exceed locs(σ) plus available location in F*

$\forall \sigma_1, \iota, \delta, \kappa', \sigma'_1$. if $(\pi, F) \vdash (\kappa, \sigma_1) \xrightarrow{\iota, \delta} (\kappa', \sigma'_1)$ then

$$\delta.wS \cup \delta.rs \subseteq \text{locs}(\sigma_1) \cup \llbracket F - \text{dom}(\sigma_1) \rrbracket$$

2. *Footprint captures enough resource for safe execution*

$\forall \sigma_1, \sigma_2, \delta_0$. if $\neg((\pi, F) \vdash (\kappa, \sigma_1) \mapsto \mathbf{abort})$, and

$$\delta_0 = \bigcup \{ \delta \mid \exists \kappa', \sigma'_1, \iota. (\pi, F) \vdash (\kappa, \sigma_1) \xrightarrow{\iota, \delta} (\kappa', \sigma'_1) \}, \text{ and}$$

$\text{LEqPre}(\sigma_1, \sigma_2, \delta_0, F)$, then $\neg((\pi, F) \vdash (\kappa, \sigma_2) \mapsto \mathbf{abort})$

3. *Footprint captures dependency of one step*

$\forall \sigma_1, \sigma_2, \delta$. if $\text{LEqPre}(\sigma_1, \sigma_2, \delta, F)$ then for any $\kappa', \iota, \sigma'_1, (\pi, F) \vdash (\kappa, \sigma_1) \xrightarrow{\iota, \delta} (\kappa', \sigma'_1)$ implies

$$\exists \sigma'_2. (\pi, F) \vdash (\kappa, \sigma_2) \xrightarrow{\iota, \delta} (\kappa', \sigma'_2) \text{ and } \text{LEqPost}(\sigma'_1, \sigma'_2, \delta, F)$$

4. *Write set captures the effect of one step*

$\forall \sigma_1, \iota, \delta, \kappa', \sigma'_1$. if $(\pi, F) \vdash (\kappa, \sigma_1) \xrightarrow{\iota, \delta} (\kappa', \sigma'_1)$ then

$$\text{LEffect}(\sigma_1, \sigma'_1, \delta, F)$$

These definitions are not sufficient to prove condition A_0 . Counter example:

$$\frac{\delta.rs \subseteq \text{locs}(\sigma)}{(_, _) \vdash (\text{nop}, \sigma) \xrightarrow{\tau, \delta} (\mathbf{skip}, \sigma)} \text{ Bad footprint}$$

Fix1 change wd-fp condition 1 to

1. *Footprint has this form of "frame property"*

$\forall \sigma, \sigma_1, \sigma_2$. if $\sigma_1 \perp \sigma_2$, $\sigma = \sigma_1 \cdot \sigma_2$, then either $(\pi, F) \vdash (\kappa, \sigma_1) \mapsto \mathbf{abort}$ or

$$\bigcup \{ \delta \mid \exists \kappa', \sigma', \iota. (\pi, F) \vdash (\kappa, \sigma) \xrightarrow{\iota, \delta} (\kappa', \sigma'_1) \} \subseteq \bigcup \{ \delta \mid \exists \kappa', \sigma'_1, \iota. (\pi, F) \vdash (\kappa, \sigma_1) \xrightarrow{\iota, \delta} (\kappa', \sigma'_1) \}$$

Then wd-lang condition 2 and 3 are redundant.

The fixed version implies the original wd (Definition 1).

Fix2 add condition

5. *Without resource captured by footprint would abort*

$\forall \sigma, \sigma_1, \sigma_2, \iota, \delta, \kappa', \sigma'$. if $\sigma_1 \perp \sigma_2$, $\sigma = \sigma_1 \cdot \sigma_2$, $(\pi, F) \vdash (\kappa, \sigma) \xrightarrow{\iota, \delta} (\kappa', \sigma')$, and $\delta \cap \text{locs}(\sigma_2) \neq \emptyset$ then $(\pi, F) \vdash (\kappa, \sigma_1) \mapsto \mathbf{abort}$

The fixed version implies the original wd (Definition 1).

12. The Footprint-Preserving Compositional Simulation

12.1 Definition of the Module-Local Simulation

$$\mu \stackrel{\text{def}}{=} (\mathbb{S}, S, f), \text{ where } \mathbb{S}, S \in \mathcal{P}(\text{BlockID}) \text{ and } f \in \text{BlockID} \rightarrow \text{BlockID} \times \mathbb{N}$$

Definition 27 (Module-Local Downward Simulation).

$(sl, \gamma) \preceq_{\mu, \varphi} (tl, \pi)$ iff $\text{dom}(sl.\text{InitCore}(\gamma)) = \text{dom}(tl.\text{InitCore}(\pi))$ and $\text{dom}(sl.\text{InitCore}(\gamma)(f)) = \mathbb{k}$, $tl.\text{InitCore}(\pi)(f) = \kappa$, $ge \subseteq \text{locs}(\Sigma)$, $\text{dom}(\Sigma) = \mathbb{S} \subseteq \text{dom}(\varphi)$, $\text{cl}(\mathbb{S}, \Sigma) = \mathbb{S}$, $\text{locs}(\sigma) = \varphi(\llbracket \text{locs}(\Sigma) \rrbracket)$, $\text{Inv}(\varphi, \Sigma, \sigma) = \mathbb{F} \cap \mathbb{S} = F \cap \varphi(\llbracket \mathbb{S} \rrbracket) = \emptyset$ and $\mu = (\mathbb{S}, \varphi(\llbracket \mathbb{S} \rrbracket), \varphi|_{\mathbb{S}})$, then there exists $i \in \text{index}$ such that $((\gamma, \mathbb{F}), (\mathbb{k}, \Sigma), \bullet) \preceq_{\mu}^{(i, (\text{emp}, \text{emp}))} ((\pi, F), (\kappa, \sigma), \bullet)$.

Here we define $((\gamma, \mathbb{F}), (\mathbb{k}, \Sigma), \beta) \preceq_{\mu}^{(i, (\Delta_0, \delta_0))} ((\pi, F), (\kappa, \sigma), \beta)$ (where the bit β is either \circ or \bullet) as the largest relation such that whenever $((\gamma, \mathbb{F}), (\mathbb{k}, \Sigma), \beta) \preceq_{\mu}^{(i, (\Delta_0, \delta_0))} ((\pi, F), (\kappa, \sigma), \beta)$, then

$\text{wf}(\mu, \Sigma)$ iff
 $\text{injective}(\mu.f, \Sigma) \wedge (\mu.S \subseteq \text{dom}(\mu.f)) \wedge (\mu.f\{\mu.S\} \subseteq \mu.S)$

$\text{wf}(\mu, \Sigma, \mathbb{F}, F)$ iff
 $\text{wf}(\mu, \Sigma) \wedge (\mu.f\{\mu.S \cap \mathbb{F}\} \subseteq F) \wedge (\mu.f\{\mu.S - \mathbb{F}\} \cap F = \emptyset)$

$\text{FPmatch}(\Delta, \delta, \mu, F)$ iff
 $(\delta.rs \cup \delta.ws \subseteq \llbracket F \cup \mu.f\{\mu.S\} \rrbracket) \wedge$
 $(\delta.rs \cap \llbracket \mu.f\{\mu.S\} \rrbracket \subseteq \mu.f\{\Delta.rs\}) \wedge$
 $(\delta.ws \cap \llbracket \mu.f\{\mu.S\} \rrbracket \subseteq \mu.f\{\Delta.ws\})$

$\text{EvolveG}(\mu, \mu', \Sigma', \sigma', \mathbb{F}, F)$ iff
 $\text{Evolve}(\mu, \mu', \Sigma', \sigma', \mathbb{F}, F) \wedge (\mu'.S - \mu.S \subseteq \mathbb{F})$

$\text{EvolveR}(\mu, \mu', \Sigma', \sigma', \mathbb{F}, F)$ iff
 $\text{Evolve}(\mu, \mu', \Sigma', \sigma', \mathbb{F}, F) \wedge ((\mu'.S - \mu.S) \cap \mathbb{F} = \emptyset)$

$\text{Evolve}(\mu, \mu', \Sigma', \sigma', \mathbb{F}, F)$ iff
 $\text{wf}(\mu', \Sigma', \mathbb{F}, F) \wedge (\mu.f \subseteq \mu'.f) \wedge \text{Inv}(\mu'.f, \Sigma', \sigma') \wedge$
 $\mu'.S = \text{cl}(\mu.S, \Sigma') \subseteq \text{dom}(\Sigma') \wedge \mu'.S = \text{cl}(\mu.S, \sigma') \subseteq \text{dom}(\sigma')$

$\text{Inv}(f, \Sigma, \sigma)$ iff
 $\forall b, n, b', n'. ((b, n) \in \text{locs}(\Sigma)) \wedge (f\langle (b, n) \rangle = (b', n')) \implies$
 $((b', n') \in \text{locs}(\sigma)) \wedge (\Sigma(b)(n) \xrightarrow{f} \sigma(b')(n'))$

$v_1 \xrightarrow{f} v_2$ iff
 $(v_1 \notin \mathfrak{U}) \wedge (v_1 = v_2) \vee v_1 \in \mathfrak{U} \wedge v_2 \in \mathfrak{U} \wedge f\langle v_2 \rangle = v_2$

$\text{injective}(f, \Sigma)$ iff
 $\forall l_1, l_2, l'_1, l'_2. l_1 \neq l_2 \wedge l_1 \in \text{locs}(\Sigma) \wedge l_2 \in \text{locs}(\Sigma) \wedge$
 $f\langle l_1 \rangle = l'_1 \wedge f\langle l_2 \rangle = l'_2 \implies l'_1 \neq l'_2$

$f\{\mathbb{S}\} \stackrel{\text{def}}{=} \{b' \mid \exists b. (b \in \mathbb{S}) \wedge f(b) = (b', -)\}$

$f\langle l \rangle \stackrel{\text{def}}{=} (b', n + n')$, if $l = (b, n) \wedge f(b) = (b', n')$

$f\langle ws \rangle \stackrel{\text{def}}{=} \{l' \mid \exists l. (l \in ws) \wedge f\langle l \rangle = l'\}$

$f|_{\mathbb{S}} \stackrel{\text{def}}{=} \{(b, f(b)) \mid b \in (\mathbb{S} \cap \text{dom}(f))\}$

$\text{cl}(\mathbb{S}, \Sigma) \stackrel{\text{def}}{=} \bigcup_k \text{cl}_k(\mathbb{S}, \Sigma)$, where $\text{cl}_k(\mathbb{S}, \Sigma)$ is inductively defined:
 $\text{cl}_0(\mathbb{S}, \Sigma) \stackrel{\text{def}}{=} \mathbb{S}$
 $\text{cl}_{k+1}(\mathbb{S}, \Sigma) \stackrel{\text{def}}{=} \{b' \mid \exists b, n, n'. (b \in \text{cl}_k(\mathbb{S}, \Sigma)) \wedge \Sigma(b)(n) = (b', n')\}$

Figure 18. Footprint Matching and Evolution of μ in Our Simulation

$\text{wf}(\mu, \Sigma, \mathbb{F}, F)$, $\text{Inv}(\mu.f, \Sigma, \sigma)$, $\mu.S = \text{cl}(\mu.S, \Sigma) \subseteq \text{dom}(\Sigma)$,
 $\mu.S = \text{cl}(\mu.S, \sigma) \subseteq \text{dom}(\sigma)$ and the following are true:

1. $\forall \kappa', \Sigma', \Delta$. if $\beta = \circ$ and $(\gamma, \mathbb{F}) \vdash (\mathbb{k}, \Sigma) \xrightarrow{\tau}_{\Delta} (\kappa', \Sigma')$, then one of the following holds:
 - (a) there exists j such that
 - i. $j < i$, and
 - ii. $((\gamma, \mathbb{F}), (\mathbb{k}', \Sigma'), \circ) \preceq_{\mu'}^{(j, (\Delta_0 \cup \Delta, \delta_0))} ((\pi, F), (\kappa, \sigma), \circ)$;
 - (b) or, there exist κ', σ', δ and j such that
 - i. $(\pi, F) \vdash (\kappa, \sigma) \xrightarrow{\tau}_{\delta}^+ (\kappa', \sigma')$, and
 - ii. $\text{FPmatch}(\Delta_0 \cup \Delta, \delta_0 \cup \delta, \mu, F)$, and
 - iii. $((\gamma, \mathbb{F}), (\mathbb{k}', \Sigma'), \circ) \preceq_{\mu'}^{(j, (\Delta_0 \cup \Delta, \delta_0 \cup \delta))} ((\pi, F), (\kappa', \sigma'), \circ)$;
2. $\forall \kappa', \iota$. if $\beta = \circ$, $\iota \neq \tau$ and $(\gamma, \mathbb{F}) \vdash (\mathbb{k}, \Sigma) \xrightarrow{\iota}_{\text{emp}} (\kappa', \Sigma)$, then there exist $\kappa', \delta, \sigma', \kappa', \mu'$ and j such that
 - (a) $(\pi, F) \vdash (\kappa, \sigma) \xrightarrow{\tau}_{\delta}^* (\kappa', \sigma')$, and
 $(\pi, F) \vdash (\kappa', \sigma') \xrightarrow{\iota}_{\text{emp}} (\kappa'', \sigma')$, and
 - (b) $\Delta_0.rs \cup \Delta_0.ws \subseteq \llbracket \mathbb{F} \cup \mu.S \rrbracket$ and

- (c) $\text{FPmatch}(\Delta_0, \delta_0 \cup \delta, \mu, F)$, and
 - (d) $\text{EvolveG}(\mu, \mu', \Sigma, \sigma', \mathbb{F}, F)$, and
 - (e) $((\gamma, \mathbb{F}), (\mathbb{k}', \Sigma), \bullet) \preceq_{\mu'}^{(j, (\text{emp}, \text{emp}))} ((\pi, F), (\kappa'', \sigma'), \bullet)$
or $\iota = \text{halt}$;
3. $\forall \sigma', \Sigma', \mu'$, if $\beta = \bullet$ and
- (a) $\Sigma \xrightarrow{\llbracket \mathbb{F} - \mu.S \rrbracket} \Sigma', \sigma \xrightarrow{\llbracket F \rrbracket - \mu.f(\text{locs}(\Sigma) \cap \llbracket \mu.S \rrbracket)} \sigma'$,
forward (Σ, Σ') , forward (σ, σ') , and
 - (b) $\text{EvolveR}(\mu, \mu', \Sigma', \sigma', \mathbb{F}, F)$,
- then there exists j such that
 $((\gamma, \mathbb{F}), (\mathbb{k}, \Sigma'), \circ) \preceq_{\mu'}^{(j, (\text{emp}, \text{emp}))} ((\pi, F), (\kappa, \sigma'), \circ)$.

13. The Footprint-Preserving Compositional Simulation (with EFCalls)

14. The Footprint-Preserving Compositional Simulation

In this section, we define a module-local simulation as the correctness obligation of each module's compilation, which is compositional and preserves footprints, allowing us to derive a whole-program simulation that preserves NPDRF. We will discuss compositionality in Sec. 6.2 and postpone the discussions of DRF and NPDRF preservation to Sec. 7.

14.1 Definition of the Module-Local Simulation

As informally explained in Sec. 2, the simulation establishes a consistency relation between executions of the source module γ and the target one π . To achieve compositionality, our simulation is also parameterized with rely/guarantee conditions, specifying the interactions between the current module and its environment at synchronization points. The consistency relation should be preserved under the environment transitions allowed in the rely condition.

Before explaining the simulation definition in Def. 28, we first define some key conditions in Fig. 19. Our simulation is parameterized with μ , defined as follows.

$$\mu \stackrel{\text{def}}{=} (\mathbb{S}, S, f),$$

where $\mathbb{S}, S \in \mathcal{P}(\text{BlockID})$ and $f \in \text{BlockID} \rightarrow \text{BlockID}$

Here S and \mathbb{S} specify the memory locations that are currently shared or were once shared. Keeping track of S and \mathbb{S} allows us to define the fixed specifications of rely/guarantee conditions in our simulation. The mapping f maps shared locations (including those once shared) at the source level to shared locations at the target. We require it to be an injective function, i.e., different source locations should be mapped to different target locations.

We require μ be well-formed with respect to the current thread's free spaces \mathbb{F} and F at the source and target levels. As defined in Fig. 19, $\text{wf}(\mu, \mathbb{F}, F)$ says that the injective function f in μ maps shared locations (in \mathbb{S}) to shared locations (in S). In particular, a location in \mathbb{F} that is shared (i.e., that has been exported to other threads) must be mapped to a shared location in F . Here $f\{\{\mathbb{S}\}\}$ (defined at the bottom of the figure) returns the set of target locations that are mapped from locations in \mathbb{S} . $f\{\{\mathbb{F} \cap \mathbb{S}\}\}$ is defined similarly.

$\text{FPmatch}(\Delta, \delta, \mu, F)$ relates the footprints Δ and δ at source and target levels. It says, every location accessed at the target (i.e., in δ) must either be from the current thread F , or correspond to a shared location at the source. For the latter case, the location must be accessed at the source (i.e., in Δ).

In our simulation μ may change after related steps at the source and target levels. Fig. 19 defines how μ is allowed to evolve to μ' under steps of the current thread (see EvolveG) and under the environment steps (see EvolveR). First, as defined in Evolve , the new μ' should be well-formed. The mapping f in μ' should relate the new states σ' and Σ' using Inv . Here Inv requires that the locations related by f be contained in the states, and the contents of these locations be also related (see \xrightarrow{f}). Besides, S and \mathbb{S} are evolved to reachable closures in the new states σ' and Σ' respectively. We define the closure function $\text{cl}(\mathbb{S}, \Sigma)$ at the bottom of the figure, which is specialized to the CompCert memory model to simplify the presentation. It returns all the locations reachable from \mathbb{S} . Since the CompCert memory model allows pointer arithmetics within blocks, we know all the locations in a reachable block (i.e., a block in which some location is reachable) are reachable. The last condition of Evolve says that f in the original μ should be preserved in the new μ' (here function subset is defined by viewing functions as special relations). EvolveG for the current thread's steps requires that the

$$\text{HFPG}(\Delta, (\mu, \mathbb{F})) \text{ iff } \text{FPG}(\Delta, \mathbb{F}, \mu.S)$$

$$\text{LFPG}(\delta, (\mu, \Delta, F)) \text{ iff } \text{FPG}(\delta, F, \mu.S) \wedge \text{FPmatch}(\mu, \Delta, \delta)$$

$$\text{FPmatch}(\mu, \Delta, \delta) \text{ iff } (\delta.rs \cap \llbracket \mu.S \rrbracket \subseteq \mu.f\langle \Delta.rs \rangle) \wedge (\delta.ws \cap \llbracket \mu.S \rrbracket \subseteq \mu.f\langle \Delta.ws \rangle)$$

$$\text{FPG}(\Delta, \mathbb{F}, \mathbb{S}) \text{ iff } \text{blocks}(\Delta.rs \cup \Delta.ws) \subseteq \mathbb{F} \cup \mathbb{S}$$

$$\text{HG}((\Delta, \Sigma'), (\mu, \mathbb{F})) \text{ iff } \text{G}(\Delta, \Sigma', \mathbb{F}, \mu.S)$$

$$\text{LG}((\delta, \sigma'), (\mu, \Delta, \Sigma', F)) \text{ iff } \text{G}(\delta, \sigma', F, \mu.S) \wedge \text{FPmatch}(\mu, \Delta, \delta) \wedge \text{Inv}(\mu.f, \Sigma', \sigma')$$

$$\text{G}(\Delta, \Sigma', \mathbb{F}, \mathbb{S}) \text{ iff } \text{FPG}(\Delta, \mathbb{F}, \mathbb{S}) \wedge (\mathbb{S} = \text{cl}(\mathbb{S}, \Sigma'))$$

$$\text{Rely}(\mu, (\Sigma, \Sigma', \mathbb{F}), (\sigma, \sigma', F)) \text{ iff } \text{R}(\Sigma, \Sigma', \mathbb{F}, \mu.S) \wedge \text{R}(\sigma, \sigma', F, \mu.S) \wedge \text{Inv}(\mu.f, \Sigma', \sigma')$$

$$\text{R}(\Sigma, \Sigma', \mathbb{F}, \mathbb{S}) \text{ iff } (\Sigma \stackrel{\llbracket \mathbb{F} \rrbracket}{=} \Sigma') \wedge (\mathbb{S} = \text{cl}(\mathbb{S}, \Sigma')) \wedge \text{forward}(\Sigma, \Sigma') \wedge ((\text{dom}(\Sigma') - \text{dom}(\Sigma)) \cap \mathbb{F} = \emptyset)$$

$$\text{Inv}(f, \Sigma, \sigma) \text{ iff } \forall b, n, b', n'. ((b, n) \in \text{locs}(\Sigma)) \wedge (f\langle (b, n) \rangle = (b', n')) \implies ((b', n') \in \text{locs}(\sigma)) \wedge (\Sigma(b)(n) \xrightarrow{f} \sigma(b')(n'))$$

$$v_1 \xrightarrow{f} v_2 \text{ iff } (v_1 \notin \mathbb{A}) \wedge (v_1 = v_2) \vee v_1 \in \mathbb{A} \wedge v_2 \in \mathbb{A} \wedge f\langle v_1 \rangle = v_2$$

$$\vec{v} \xrightarrow{f} \vec{v}' \text{ iff } \exists v_1, v'_1, \dots, v_n, v'_n. \vec{v} = \{v_1, \dots, v_n\} \wedge \vec{v}' = \{v'_1, \dots, v'_n\} \wedge \forall i. v_i \xrightarrow{f} v'_i$$

$$\iota \xrightarrow{f} \iota' \text{ iff } \iota = \iota' = e \vee \iota = \iota' = \text{entA} \vee \iota = \iota' = \text{extA} \vee \exists \mathbf{f}, \vec{v}, \vec{v}'. \iota = \text{call}(\mathbf{f}, \vec{v}) \wedge \iota' = \text{call}(\mathbf{f}, \vec{v}') \wedge \vec{v} \xrightarrow{f} \vec{v}' \vee \exists v, v'. \iota = \text{ret}(v) \wedge \iota' = \text{ret}(v') \wedge v \xrightarrow{f} v'$$

$$\text{Gargs}(\iota, \mathbb{S}) \text{ iff } \iota = e \vee \iota = \text{entA} \vee \iota = \text{extA} \vee \exists \mathbf{f}, \vec{v}. \iota = \text{call}(\mathbf{f}, \vec{v}) \wedge (\forall b, n. (b, n) \in \vec{v} \implies b \in \mathbb{S}) \vee \exists v. \iota = \text{ret}(v) \wedge (\forall b, n. v = (b, n) \implies b \in \mathbb{S})$$

$$\text{injective}(f) \text{ iff } \forall b_1, b_2, b'_1, b'_2. b_1 \neq b_2 \wedge f(b_1) = b'_1 \wedge f(b_2) = b'_2 \implies b'_1 \neq b'_2$$

$$f\{\{\mathbb{S}\}\} \stackrel{\text{def}}{=} \{b' \mid \exists b. (b \in \mathbb{S}) \wedge f(b) = b'\}$$

$$f\langle l \rangle \stackrel{\text{def}}{=} (b', n), \text{ if } l = (b, n) \wedge f(b) = b'$$

$$f\langle ws \rangle \stackrel{\text{def}}{=} \{l' \mid \exists l. (l \in ws) \wedge f\langle l \rangle = l'\}$$

$$f|_{\mathbb{S}} \stackrel{\text{def}}{=} \{(b, f(b)) \mid b \in (\mathbb{S} \cap \text{dom}(f))\}$$

$$f_2 \circ f_1 \stackrel{\text{def}}{=} \{(b_1, b_3) \mid \exists b_2. f_1(b_1) = b_2 \wedge f_2(b_2) = b_3\}$$

$$\text{cl}(\mathbb{S}, \Sigma) \stackrel{\text{def}}{=} \bigcup_k \text{cl}_k(\mathbb{S}, \Sigma), \text{ where } \text{cl}_k(\mathbb{S}, \Sigma) \text{ is inductively defined:}$$

$$\text{cl}_0(\mathbb{S}, \Sigma) \stackrel{\text{def}}{=} \mathbb{S}$$

$$\text{cl}_{k+1}(\mathbb{S}, \Sigma) \stackrel{\text{def}}{=} \{b' \mid \exists b, n, n'. (b \in \text{cl}_k(\mathbb{S}, \Sigma)) \wedge \Sigma(b)(n) = (b', n')\}$$

Figure 19. Footprint Matching and Rely/Guarantee Conditions in Our Simulation

additional locations in $\mu'.f$ be allocated from the current thread's \mathbb{F} , while EvolveR for the environment steps says the opposite.

Definition 28 (Module-Local Downward Simulation).

$(sl, \gamma) \preceq_{ge, \varphi} (tl, \pi)$ iff
 $\text{dom}(sl.\text{InitCore}(\gamma)) = \text{dom}(tl.\text{InitCore}(\pi))$ and
for any $\mathbf{f}, \kappa, \mathbb{k}, \sigma, \Sigma, \mu, \mathbb{F}, F, \mathbb{S}$ and S , if $sl.\text{InitCore}(\gamma)(\mathbf{f}) = \mathbb{k}$,
 $tl.\text{InitCore}(\pi)(\mathbf{f}) = \kappa$, $ge \subseteq \text{locs}(\Sigma)$, $\text{dom}(\Sigma) = \mathbb{S} = \text{cl}(\mathbb{S}, \Sigma)$,
 $\text{dom}(\sigma) = S = \varphi\{\mathbb{S}\}$, $\text{locs}(\sigma) = \varphi\{\text{locs}(\Sigma)\}$, $\text{Inv}(\varphi, \Sigma, \sigma)$,
 $\mathbb{F} \cap \mathbb{S} = F \cap S = \emptyset$ and $\mu = (\mathbb{S}, S, \varphi|_{\mathbb{S}})$, then there exists $i \in \text{index}$
such that $((\gamma, \mathbb{F}), (\mathbb{k}, \Sigma), \bullet) \preceq_{\mu}^{(i, (\text{emp}, \text{emp}))} ((\pi, F), (\kappa, \sigma), \bullet)$.

Here we define $((\gamma, \mathbb{F}), (\mathbb{k}, \Sigma), \beta) \preceq_{\mu}^{(i, (\Delta_0, \delta_0))} ((\pi, F), (\kappa, \sigma), \beta)$
(where the bit β is either \circ or \bullet) as the largest relation such that
whenever $((\gamma, \mathbb{F}), (\mathbb{k}, \Sigma), \beta) \preceq_{\mu}^{(i, (\Delta_0, \delta_0))} ((\pi, F), (\kappa, \sigma), \beta)$, then
the following are true:

1. $\forall \mathbb{k}', \Sigma', \Delta$. if $\beta = \circ$, $(\gamma, \mathbb{F}) \vdash (\mathbb{k}, \Sigma) \xrightarrow{\tau}_{\Delta} (\mathbb{k}', \Sigma')$ and
 $\text{HFPG}(\Delta_0 \cup \Delta, (\mu, \mathbb{F}))$, then one of the following holds:
 - (a) there exists j such that
 - i. $j < i$, and
 - ii. $((\gamma, \mathbb{F}), (\mathbb{k}', \Sigma'), \circ) \preceq_{\mu}^{(j, (\Delta_0 \cup \Delta, \delta_0))} ((\pi, F), (\kappa, \sigma), \circ)$;
 - (b) or, there exist κ', σ', δ and j such that
 - i. $(\pi, F) \vdash (\kappa, \sigma) \xrightarrow{\tau}_{\delta}^+ (\kappa', \sigma')$, and
 - ii. $\text{LFPG}(\delta_0 \cup \delta, (\mu, \Delta_0 \cup \Delta, F))$, and
 - iii. $((\gamma, \mathbb{F}), (\mathbb{k}', \Sigma'), \circ) \preceq_{\mu}^{(j, (\Delta_0 \cup \Delta, \delta_0 \cup \delta))} ((\pi, F), (\kappa', \sigma'), \circ)$;
2. $\forall \mathbb{k}', \iota$. if $\beta = \circ$, $\iota \neq \tau$, $(\gamma, \mathbb{F}) \vdash (\mathbb{k}, \Sigma) \xrightarrow{\iota}_{\text{emp}} (\mathbb{k}', \Sigma)$ and
 $\text{HG}((\Delta_0, \Sigma), (\mu, \mathbb{F}))$ and $\text{Gargs}(\iota, \mu.\mathbb{S})$, then there exist κ', δ ,
 $\iota', \sigma', \kappa''$ and j such that
 - (a) $(\pi, F) \vdash (\kappa, \sigma) \xrightarrow{\tau}_{\delta}^* (\kappa', \sigma')$, and
 $(\pi, F) \vdash (\kappa', \sigma') \xrightarrow{\iota'}_{\text{emp}} (\kappa'', \sigma')$, and
 - (b) $\text{LG}((\delta_0 \cup \delta, \sigma'), (\mu, \Delta_0, \Sigma, F))$, and $\iota \xrightarrow{\mu, f} \iota'$, and
 - (c) $((\gamma, \mathbb{F}), (\mathbb{k}', \Sigma), \bullet) \preceq_{\mu}^{(j, (\text{emp}, \text{emp}))} ((\pi, F), (\kappa'', \sigma'), \bullet)$
or $\iota = \text{ret}(v)$;
3. $\forall \sigma', \Sigma', v_s, \mathbb{k}', v_t$, if $\beta = \bullet$ and $\text{AftExtn}(\mathbb{k}, v_s) = \mathbb{k}'$ and
 $v_s \xrightarrow{\mu, f} v_t \text{Rely}(\mu, (\Sigma, \Sigma', \mathbb{F}), (\sigma, \sigma', F))$,
then there exists κ', j such that
 - (a) $\text{AftExtn}(\kappa, v_t) = \kappa'$, and
 - (b) $((\gamma, \mathbb{F}), (\mathbb{k}', \Sigma'), \circ) \preceq_{\mu}^{(j, (\text{emp}, \text{emp}))} ((\pi, F), (\kappa', \sigma'), \circ)$.

The simulation $(sl, \gamma) \preceq (tl, \pi)$ relates the executions of the
source module γ and the target module π . We first use the languages'
 InitCore functions (see Fig. 4) to initialize the ‘‘core’’ states \mathbb{k} and
 κ . We assume their executions start from the same initial memory
states at the source and target levels, and the whole memory is shared
between the current thread and its environment. We define μ with
the injective function $\text{id}_{\mathbb{S}}$, which is the identity function on \mathbb{S} . That
is, $\text{dom}(\text{id}_{\mathbb{S}}) = \mathbb{S}$ and $\forall l \in \mathbb{S}. \text{id}_{\mathbb{S}}(l) = l$.

The index i and the history footprints (Δ_0, δ_0) are introduced to
ensure footprint preservation. Since compilations may reorder in-
structions, it may be more natural to relate the footprints of *multiple*
source steps to the corresponding target footprints. Our simulation
allows one to accumulate the footprints using the history footprints
 (Δ_0, δ_0) , and check FPmatch (the footprint matching condition,
defined in Fig. 19) later. Note that one may choose to check FPmatch
at every source step. Whether to accumulate footprints or check
 FPmatch depends on the compilation applications, and we leave
the choices to verifiers. However, for infinite executions, it is not
allowed to continuously accumulate the footprints and never check
 FPmatch . Therefore our simulation is parameterized with a metric
 i from a well-founded set index . The metric should decrease at

steps that accumulate the footprints and could be reset at steps that
check FPmatch .

We also introduce a bit β to indicate the synchronization points
(i.e., the program points when the control may switch to the
environment). It takes two values \bullet and \circ . Initially it is \bullet , indicating
a possible switch that allows the environment threads to make steps
before the current thread starts. Internal τ -steps of the current thread
keeps β to be \circ (see condition 1 in Def. 28). When the current thread
makes a transition labeled with e , entA or extA (see condition 2 in
Def. 28), we set β from \circ to \bullet . The environment can interfere with
the current thread when β is \bullet (see condition 3 in Def. 28). After
the environment interference, β should be reset to \circ .

Our simulation definition follows the diagram in Fig. 2(b). Every
source τ -step should correspond to zero-or-multiple target τ -steps
(see condition 1 in Def. 28). One may check FPmatch for footprints
accumulated till the current steps and reset the metric (see 1(a)),
or choose to continue accumulating the footprints and decrease the
metric (see 1(b)). To simplify the presentation, $[[[?]]]$ the footprints
should be accumulated when the source step correspond to zero
target steps.

At switch points (see condition 2 in Def. 28), we check FPmatch
for the accumulated footprints and reset the metric. We also evolve
 μ to μ' , which satisfies EvolveG (defined in Fig. 19).

We require the simulation relation is preserved after the envi-
ronment interference (see condition 3 in Def. 28). The environment
steps should not change the current thread's local memory. The con-
dition 3(a) disallow the environment threads to update locations in
the current thread's \mathbb{F} (or F at the target level) except in the shared
parts \mathbb{S} (or corresponds to \mathbb{S}). Here \equiv is defined at the bottom of
Fig. 4. After the environment step, μ is evolved to μ' satisfying
 EvolveR (defined in Fig. 19).